



SERVICE MANUAL

VHF MARINE TRANSCEIVER

IC-M411

S-14414XZ-C1
Nov. 2007

Icom Inc.

INTRODUCTION

This service manual describes the latest service information for the **IC-M411** VHF MARINE TRANSCEIVER at the time of publication.

MODEL	VERSION	CHANNEL GROUPE
IC-M411	UK	Inter National, U.S.A.
	EUR	International
	HOL	International, ATIS
	FRG	International, DSC, ATIS
	EUR-1	International

UNIT ABBREVIATIONS:

FR= FRONT UNIT, LG= LOGIC UNIT, MA= MAIN UNIT

CAUTION

NEVER connect the transceiver to an AC outlet or to a DC power supply that uses more than specified. This will ruin the transceiver.

DO NOT reverse the polarities of the power supply when connecting the transceiver.

DO NOT apply an RF signal of more than 20 dBm (100 mW) to the antenna connector. This could damage the transceiver's front-end.

To upgrade quality, any electrical or mechanical parts and internal circuits are subject to change without notice or obligation.



ORDERING PARTS

Be sure to include the following four points when ordering replacement parts:

1. 10-digit Icom parts numbers
2. Component name
3. Equipment model name and unit name
4. Quantity required

<ORDER EXAMPLE>

1110003491 S.IC TA31136FNG IC-M411 MAIN UNIT 5 pieces
8820001210 Screw 2438 screw IC-M411 Top cover 10 pieces

Addresses are provided on the inside back cover for your convenience.

REPAIR NOTES

1. Make sure the problem is internal before disassembling the transceiver.
2. **DO NOT** open the transceiver until the transceiver is disconnected from its power source.
3. **DO NOT** force any of the variable components. Turn them slowly and smoothly.
4. **DO NOT** short any circuits or electronic parts. An insulated tuning tool **MUST** be used for all adjustments.
5. **DO NOT** keep power ON for a long time when the transceiver is defective.
6. **DO NOT** transmit power into a Standard Signal Generator or a Sweep Generator.
7. **ALWAYS** connect a 40 dB to 50 dB attenuator between the transceiver and a Deviation Meter or Spectrum Analyzer when using such test equipment.
8. **READ** the instructions of test equipment thoroughly before connecting a test equipment to the transceiver.

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SECTION 11 HM-164B

■ General

- Frequency coverage : Tx 156.000–161.450 MHz
Rx 156.000–163.425 MHz
- Mode : FM (16K0G3E),
DSC (16K0G2B)
- Channel spacing : 25 kHz
- Current drain (at 13.8 V) : TX high 5.5 A max.
Max. audio 1.5 A max.
- Power supply requirement: 13.8 V DC (10.8 to 15.6 V)
(negative ground)
- Frequency stability : Less than ± 1.5 kHz
- Operating temp. range : -20°C to $+60^{\circ}\text{C}$
- Antenna impedance : 50 Ω nominal
- Input impedance (MIC) : 2 k Ω
- Output impedance (audio): 4 Ω
- Dimensions : 164(W) \times 78(H) \times 139.5(D) mm
(Projections not included)
- Weight : Approx. 1080 g

■ Transmitter

- RF output power : 25 W/1 W
- Modulation system : Variable reactance frequency
modulation
- Max. frequency deviation : ± 5.0 kHz
- Spurious emissions : Less than 0.25 μW
- Adjacent channel power : More than 70 dB
- Audio harmonic distortion : Less than 10%
(at 60% deviation)
- Residual modulation : More than 40 dB
- Audio frequency response: +1 to -3 dB of 6 dB/octave
range from 300 to 3000 Hz

■ Receiver

- Receive system : Double conversion
superheterodyne
- Sensitivity (20 dB SINAD) : -5 dB μ emf (typical)
- Squelch sensitivity : Less than -2 dB μ emf
- Intermodulation rejection ratio : More than 68 dB
- Spurious response rejection ratio: More than 70 dB
- Adjacent channel selectivity : More than 70 dB
- Audio output power : More than 2.0 W
at 10% distortion
with a 4 Ω load

All stated specifications are subject to change without notice or obligation.

• **International channels**

CH	Frequency (MHz)		CH	Frequency (MHz)		CH	Frequency (MHz)		CH	Frequency (MHz)		CH	Frequency (MHz)		CH	Frequency (MHz)	
	Transmit	Receive		Transmit	Receive		Transmit	Receive		Transmit	Receive		Transmit	Receive		Transmit	Receive
01	156.050	160.650	11	156.550	156.550	21	157.050	161.650	61	156.075	160.675	71	156.575	156.575	81	157.075	161.675
02	156.100	160.700	12	156.600	156.600	22	157.100	161.700	62	156.125	160.725	72	156.625	156.625	82	157.125	161.725
03	156.150	160.750	13	156.650	156.650	23	157.150	161.750	63	156.175	160.775	73	156.675	156.675	83	157.175	161.775
04	156.200	160.800	14	156.700	156.700	24	157.200	161.800	64	156.225	160.825	74	156.725	156.725	84	157.225	161.825
05	156.250	160.850	15* ²	156.750	156.750	25	157.250	161.850	65	156.275	160.875	75* ⁴	156.775	156.775	85	157.275	161.875
06	156.300	156.300	16	156.800	156.800	26	157.300	161.900	66	156.325	160.925	76* ⁴	156.825	156.825	86	157.325	161.925
07	156.350	160.950	17* ²	156.850	156.850	27	157.350	161.950	67	156.375	156.375	77	156.875	156.875	87	157.375	157.375
08	156.400	156.400	18	156.900	161.500	28	157.400	162.000	68	156.425	156.425	78	156.925	161.525	88	157.425	157.425
09	156.450	156.450	19	156.950	161.550	37A* ³	157.850	157.850	69	156.475	156.475	79	156.975	161.575	P4* ³	161.425	161.425
10	156.500	156.500	20	157.000	161.600	60	156.025	160.625	70* ¹	156.525	156.525	80	157.025	161.625			

*¹ DSC operation only.

*² Channels 15 and 17 may also be used for on-board communications provided the effective radiated power does not exceed 1 W and subject to the national regulations of the administration concerned when these channels are used in its territorial waters.

*³ UK Marina Channels: M1=37A (157.850 MHz), M2=P4 (161.425 MHz) for U.K. version only

*⁴ The use of these channels should be restricted to navigation-related communications only and all precautions should be taken to avoid harmful interference to channel 16, e.g. by limiting the output power to 1 W or by means geographical separation.

• **USA channels** (for U.K. version only)

CH	Frequency (MHz)		CH	Frequency (MHz)		CH	Frequency (MHz)		CH	Frequency (MHz)		CH	Frequency (MHz)		CH	Frequency (MHz)	
	Transmit	Receive		Transmit	Receive		Transmit	Receive		Transmit	Receive		Transmit	Receive		Transmit	Receive
01A	156.050	156.050	12	156.600	156.600	22A	157.100	157.100	64A	156.225	156.225	75* ¹	156.775	156.775	85	157.275	161.875
--	---	---	13* ²	156.650	156.650	23A	157.150	157.150	65A	156.275	156.275	76* ¹	156.825	156.825	85A	157.275	157.275
03A	156.150	156.150	14	156.700	156.700	24	157.200	161.800	66A	156.325	156.325	77* ¹	156.875	156.875	86	157.325	161.925
--	---	---	15* ²	156.750	156.750	25	157.250	161.850	67* ²	156.375	156.375	78A	156.925	156.925	86A	157.325	157.325
05A	156.250	156.250	16	156.800	156.800	26	157.300	161.900	68	156.425	156.425	79A	156.975	156.975	87	157.375	161.975
06	156.300	156.300	17* ¹	156.850	156.850	27	157.350	161.950	69	156.475	156.475	80A	157.025	157.025	87A	157.375	157.375
07A	156.350	156.350	18A	156.900	156.900	28	157.400	162.000	70* ³	156.525	156.525	81A	157.075	157.075	88	157.425	162.025
08	156.400	156.400	19A	156.950	156.950	37A* ⁴	157.850	157.850	71	156.575	156.575	82A	157.125	157.125	88A	157.425	157.425
09	156.450	156.450	20	157.000	161.600	61A	156.075	156.075	72	156.625	156.625	83A	157.175	157.175	P4* ⁴	161.425	161.425
10	156.500	156.500	20A	157.000	157.000	--	---	---	73	156.675	156.675	84	157.225	161.825			
11	156.550	156.550	21A	157.050	157.050	63A	156.175	156.175	74	156.725	156.725	84A	157.225	157.225			

*¹ Low power only.

*² Momentary high power.

*³ DSC operation only.

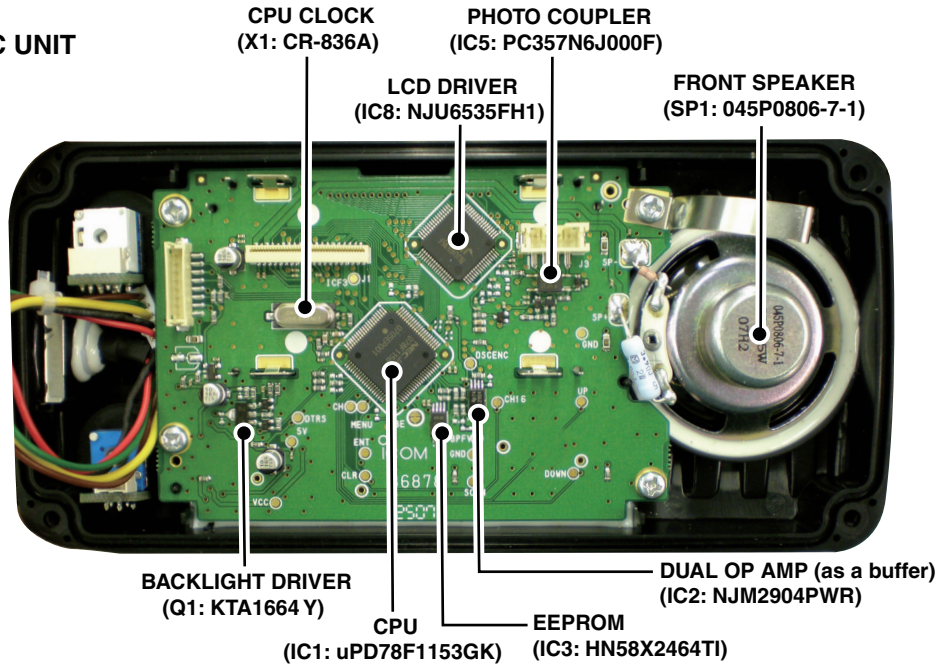
*⁴ UK Marina Channels: M1=37A (157.850 MHz), M2=P4 (161.425 MHz) for U.K. version only

NOTE: Simplex channels, 3, 21, 23, 61, 64, 81, 82 and 83 **CANNOT** be lawfully used by the general public in U.S.A. waters.

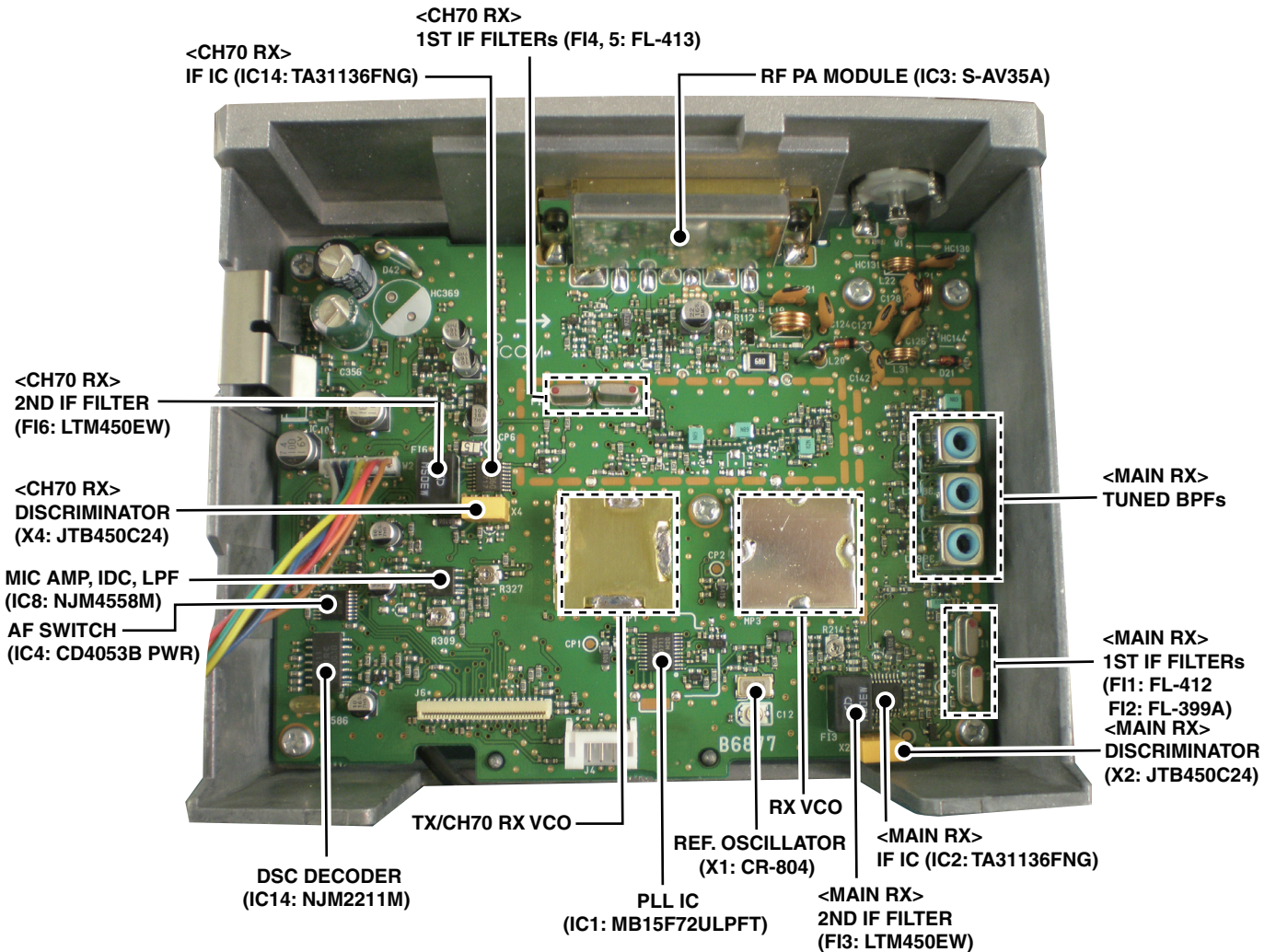
SECTION 2

INSIDE VIEWS

• LOGIC UNIT



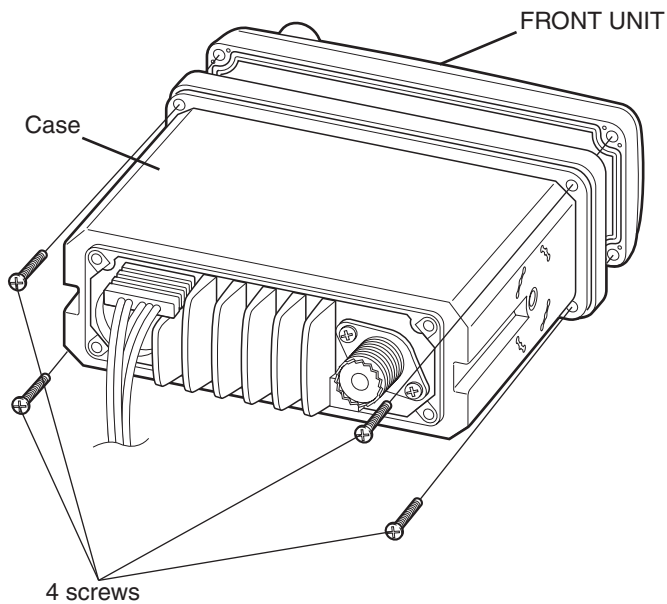
• MAIN UNIT



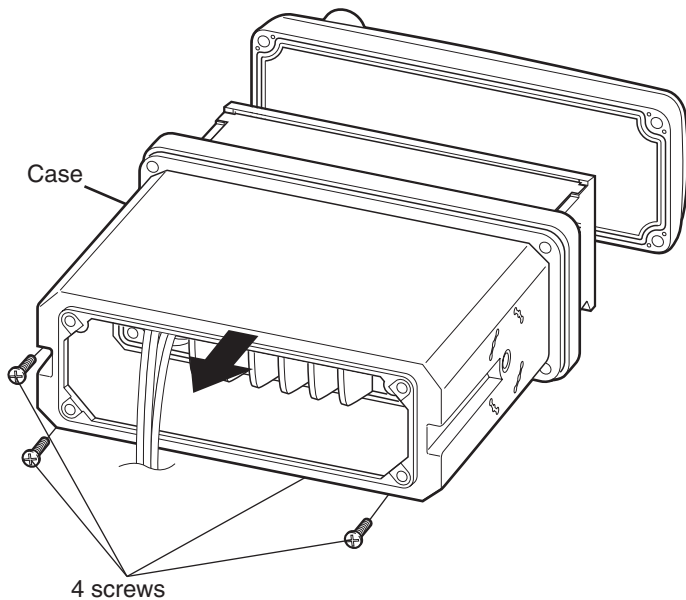
SECTION 3 DISASSEMBLY INSTRUCTION

● REMOVING THE CASE

- ① Unscrew 4 screws from the bottom of FRONT UNIT.
Note: When replacing the screws, 10–12 kg of torque MUST be applied to ensure waterproof.

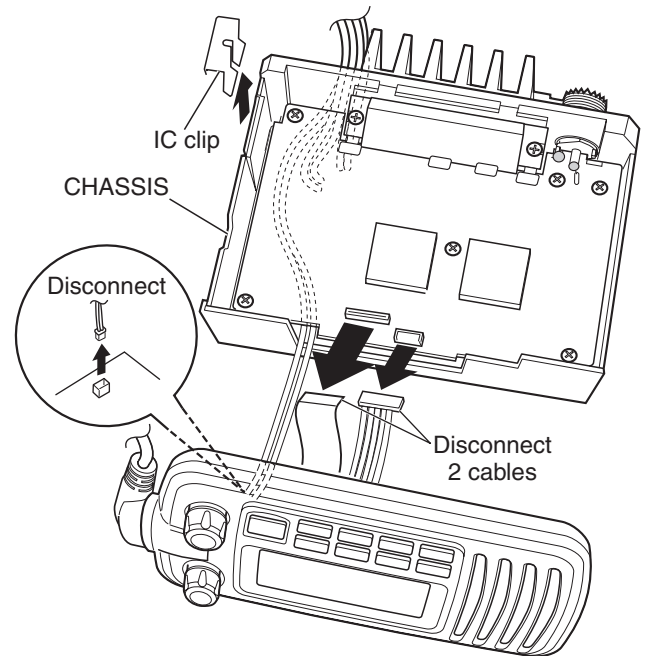


- ② Unscrew 4 screws from the rear.
Note: When replacing the screws, 10–12 kg of torque MUST be applied to ensure waterproof.
- ③ Slide the case free of the chassis in the direction of the arrow.

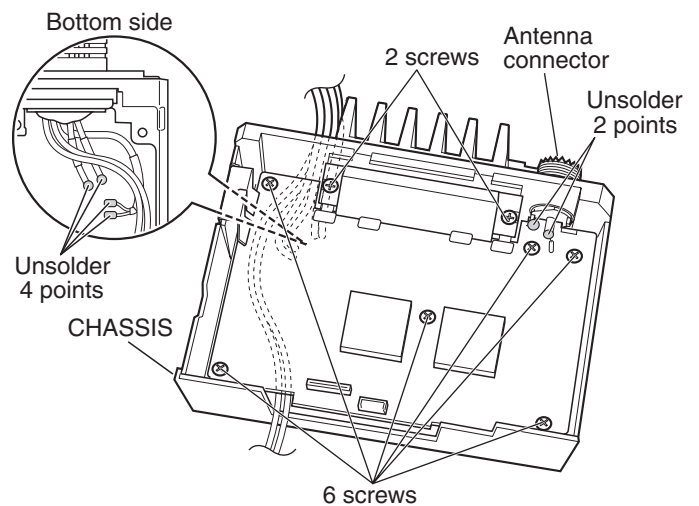


● REMOVING THE MAIN UNIT

- ① Remove the IC clip from the CHASSIS.
- ② Disconnect 2 cables from the MAIN UNIT.
- ③ Disconnect the cable from the FRONT UNIT, and remove it.



- ④ Unsolder 4 points at the bottom side of the MAIN UNIT.
- ⑤ Unsolder 2 points at the bottom of ANT connector.
- ⑥ Unscrew 2 screws from the PA module, and 6 screws from the MAIN UNIT, then remove the MAIN UNIT from the CHASSIS.



4-1 RECEIVER CIRCUITS

ANTENNA SWITCH (MAIN-RX/CH70-RX)

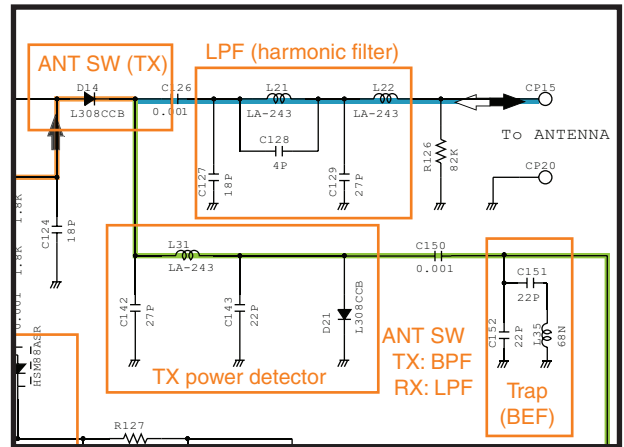
The received signals (RX signals) from the antenna are passed through the LPF (harmonic filter) and ANT SW (as an LPF in RX).

The ANT SW toggles RX line and TX line. While receiving, the TX line and antenna is disconnected to prevent RX signals entering. The RX line is disconnected from the GND simultaneously, and an LPF which guides RX signals to the RX circuits is composed.

While transmitting, serial-connected PIN diodes (D14, 21) are forward biased and the TX line and the antenna is connected by D14. Simultaneously, the RX line is connected to the GND by D21 to prevent transmit signal entering.

The RX signals from the ANT SW (=LPF) are applied to the RF circuits via the trap (BEF; Band Eliminate Filter).

• ANT SW (MAIN-RX/CH70-RX)



RF CIRCUITS (MAIN-RX)

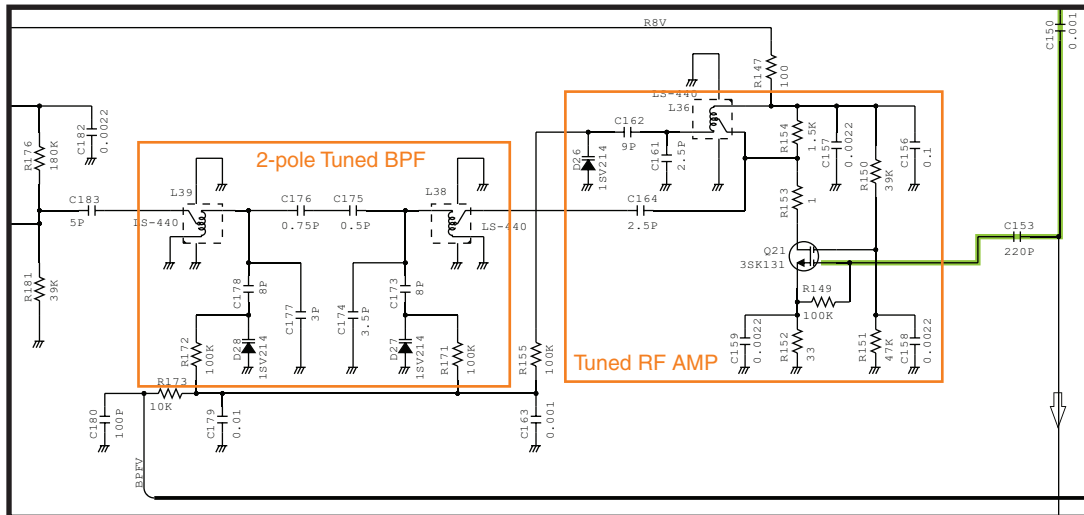
RF circuits consist of RF filters and RF amplifier (RF AMP) that filter and amplify the signals of frequency which desired to receive.

The filtered RX signals from the trap (BEF; Band Eliminate Filter) are applied to the tuned RF AMP. The amplified RX signals are passed through the tuned 2-pole BPF.

The tuned RF AMP and 2-pole BPF are adjusted so that it responds to receiving frequency and rejects all others, by the variable capacitor whose capacitance is varied by applied tuning voltage "BPFV."

The filtered RX signals are then applied to the 1st IF circuits.

• RF CIRCUITS (MAIN-RX)



UNIT ABBREVIATIONS:

FR= FRONT UNIT, LG= LOGIC UNIT, MA= MAIN UNIT

1ST IF CIRCUITS (MAIN-RX)

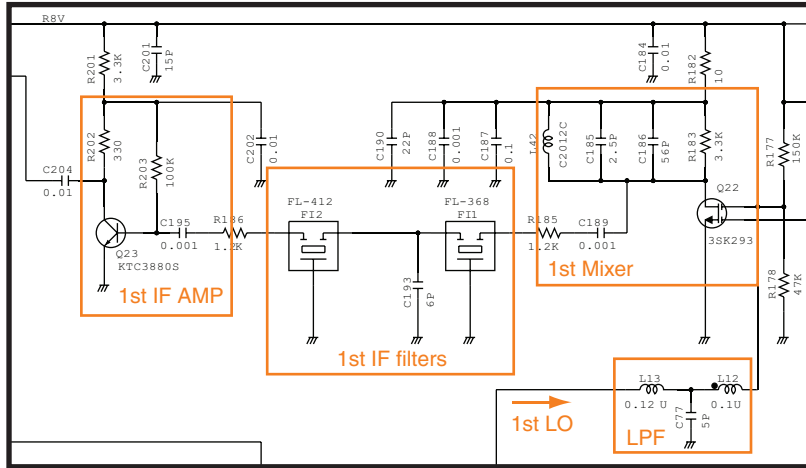
The 1st IF circuits consist of 1st mixer, 1st IF filter and 1st IF amplifier. And it down-converts the RX signals into the 1st IF signal then filters and amplifies.

The filtered RX signals from the 2-pole tuned BPF are applied to the 1st mixer to be converted into the 1st IF signal, by being mixed with the 1st Local Oscillator (LO) signals from the RX VCO (Q72, D51-53) via buffers (Q74, 76) and LPF (L12, 13, C77).

The converted 1st IF signal is passed through two 1st IF filters to remove unwanted signals. The filtered 1st IF signal is amplified by the 1st IF AMP.

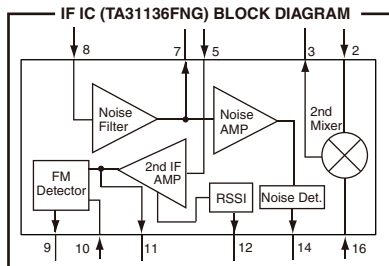
The amplified 1st IF signal is then applied to the 2nd IF circuits.

• 1ST IF CIRCUITS (MAIN-RX)



2ND IF AND DEMODULATOR CIRCUITS (MAIN-RX)

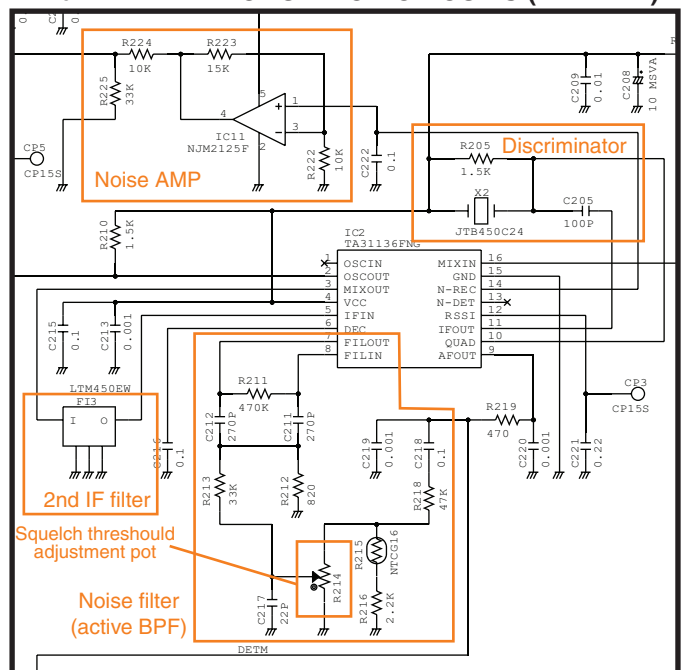
The 2nd IF circuits consist of 2nd mixer, 2nd IF filter and 2nd IF amplifier. It down-converts the 1st IF signal into the 2nd IF signal, then filters to filter 2nd IF signal only and amplifies it. The demodulator circuit converts the 2nd IF signal into AF signals. The IF IC "TA31136FNG" contains major sections of 2nd IF and FM demodulator circuits in its package.



The amplified 1st IF signal from the 1st IF AMP (Q23) is applied to the IF IC (IC2, pin 16), and converted into the 450 kHz 2nd IF signal at the internal 2nd mixer, by being mixed with the 2nd LO signal from the reference frequency oscillator (Q85, X1). The converted 2nd IF signal is output from pin 3 and filtered by the external 2nd IF filter (F13). The filtered 2nd IF signal is backed to the IF IC (pin 5), and saturation-amplified by the internal 2nd IF AMP. The amplified 2nd IF signal is FM-demodulated by the quadrature detector with a discriminator (X2).

The demodulated AF signals are applied to the RX AF circuits.

• 2nd IF AND DEMODULATOR CIRCUITS (MAIN-RX)



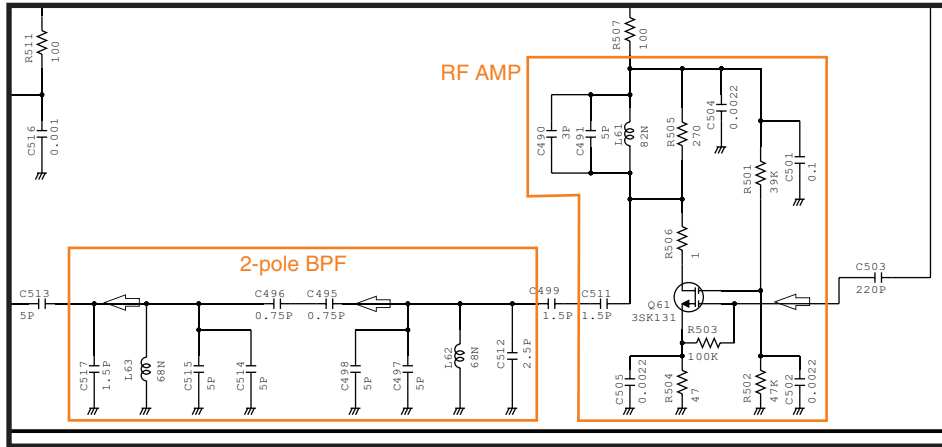
RF CIRCUITS (CH70-RX)

RF circuits for CH70-RX consist of RF filters and RF amplifier (RF AMP), and filter and amplify the signal of CH70 (156.525 MHz) only. The circuits are fixed to receive 156.525 MHz signal only, and are activated even when the transceiver is receiving other channels (MAIN-RX).

The filtered RX signal from the trap (BEF; Band Eliminate Filter) are applied to the RF AMP. The amplified RX signal is passed through the 2-pole BPF.

The filtered RX signal is then applied to the 1st IF circuits.

• RF CIRCUITS (CH70-RX)



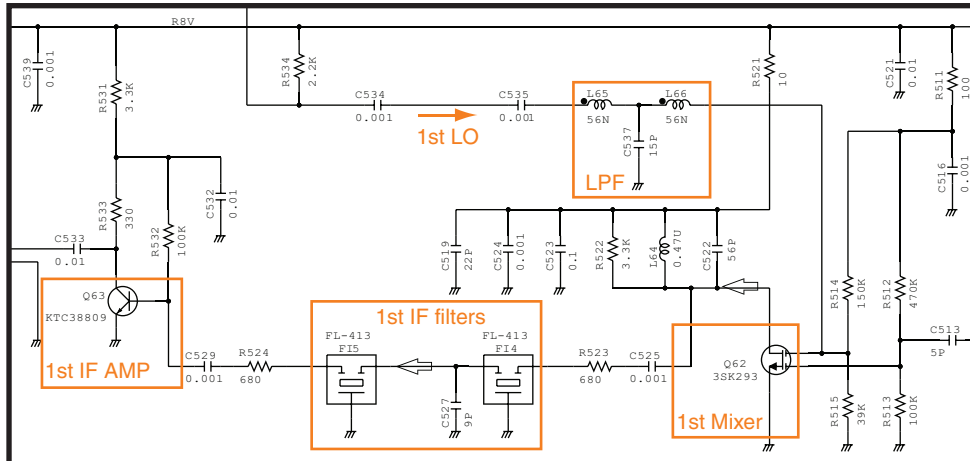
1ST IF CIRCUITS (CH70-RX)

The 1st IF circuits consist of 1st mixer, 1st IF filter and 1st IF amplifier. And it down-convert the RX signals into the 1st IF signal, then filter and amplify.

The converted 1st IF signal is passed through two 1st IF filters to remove unwanted signals. The filtered 1st IF signal is amplified by the 1st IF AMP. The amplified 1st IF signal is then applied to the 2nd IF circuits.

The filtered RX signal from the 2-pole BPF are applied to the 1st mixer to be converted into the 1st IF signal, by being mixed with the 1st Local Oscillator (LO) signals from the TX/CH70-RX VCO (Q4, 5, D1-3) via buffer (Q7), LO SW (D8) and LPF (L65, 66, C537).

• 1ST IF CIRCUITS (CH70-RX)



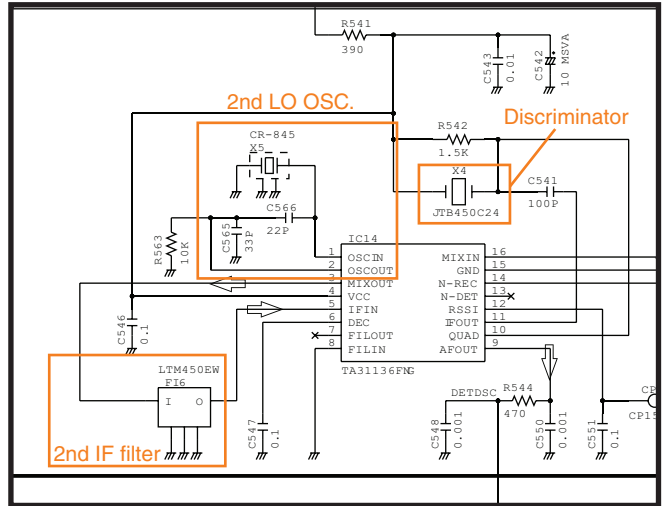
2ND IF AND DEMODULATOR CIRCUITS (CH70-RX)

The 2nd IF circuits consist of 2nd mixer, 2nd IF filter and 2nd IF amplifier, convert the 1st IF signal into the 2nd IF signal, then filter to filter 2nd IF signal only and amplify. And the demodulator circuit converts the 2nd IF signal to AF signals.

The amplified 1st IF signal is applied to the IF IC (IC14, pin 16), and converted into the 450 kHz 2nd IF signal at the internal 2nd mixer, by being mixed with the 2nd LO signal generated by the internal reference frequency oscillator (X5). The converted 2nd IF signal is output from pin 3, and filtered by the external 2nd IF filter (F16). The filtered 2nd IF signal is backed to the IF IC (pin 5), and saturation-amplified by the internal 2nd IF AMP. The amplified 2nd IF signal is FM-demodulated by the quadrature detector with a discriminator (X4).

The demodulated DSC signals are applied to the DSC decoding circuits.

• 2nd IF AND DEMODULATOR CIRCUITS (CH70-RX)



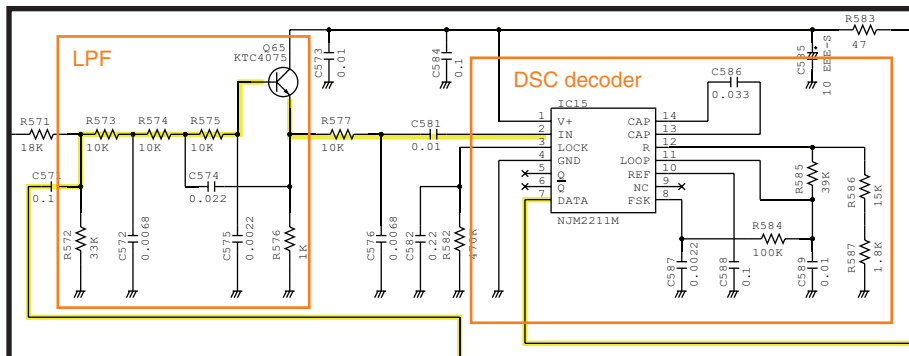
DSC DECODING CIRCUITS (CH70-RX only)

The demodulated DSC signals from the IF IC (IC14) are filtered by LPF to remove audible signals. The filtered sub audible tone signals are applied to the DSC decoder.

The decoder detects it then outputs serial data to the CPU (LG; IC1, pin 41) to control the transceiver (emergency alarm, DSC indication, etc.).

The IC15 is the DSC decode IC which converts the sub audible tone signals into the serial data (logic "H/L" status).

DSC DECODING CIRCUITS



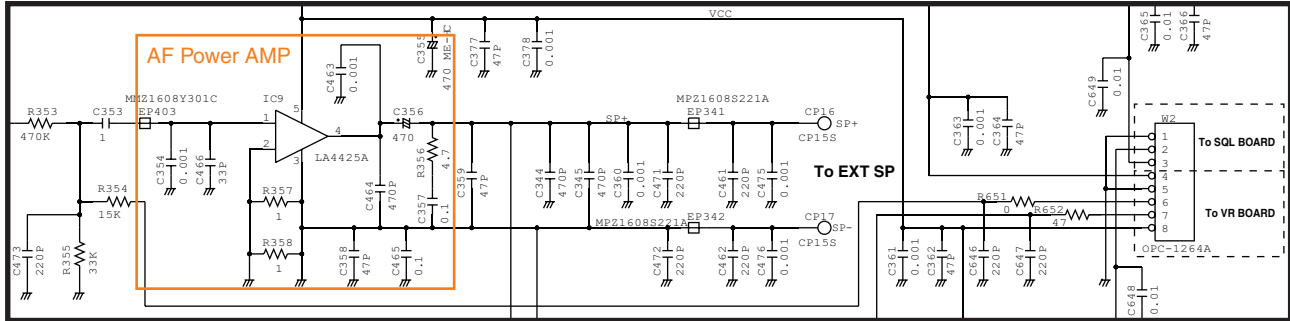
RX AF CIRCUITS (MAIN-RX only)

The RX AF circuits consist of AF filter, AF SW, AF amplifier and AF power amplifier, and amplify and filter the AF signals demodulated by the demodulator circuits.

The demodulated AF signals from the IF IC (IC2) are passed through the AF line SW (IC4, pins 13, 14) which toggles the AF lines between MAIN-RX and CH70-RX, and passed through the de-emphasis circuit (R231, C232) to obtain -6 dB/oct of frequency response.

The de-emphasized AF signals are passed through the BPF (Q31, 32) and the AF mute SW (IC4, pins 15, 1) then applied to the VR UNIT. The AF signals are adjusted its level by the variable resistor (VR: R1), and backed to the MAIN UNIT and applied to the AF power AMP (IC9). The AF signals are power-amplified to obtain more than 2 W of audio output power. The power-amplified AF signals are applied to the internal/external speaker.

• RX AF CIRCUITS (MAIN-RX only)

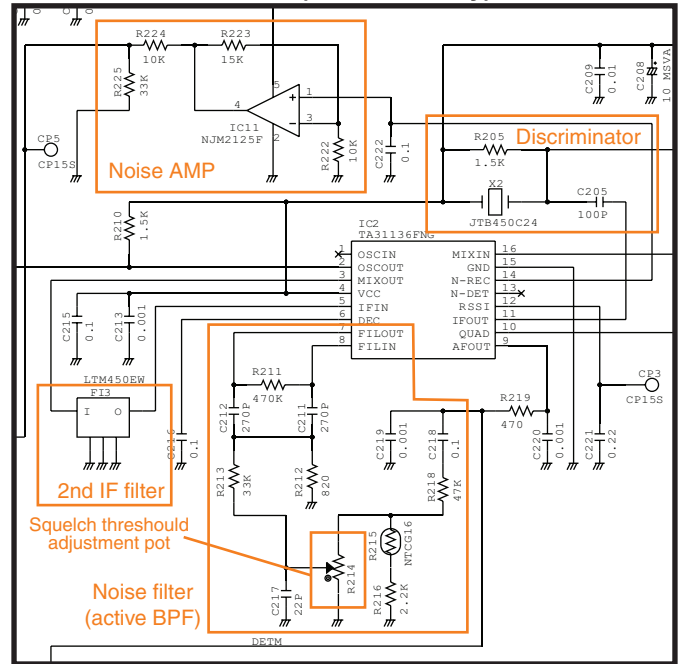


SQUELCH CIRCUIT (MAIN-RX only)

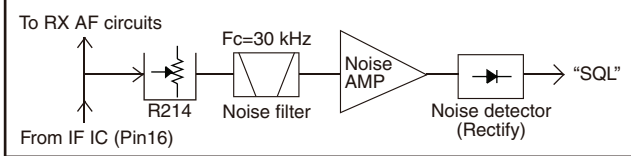
The squelch circuit cuts the AF output signals off when no RF signals are received. Filtering noise components in the demodulated AF signals, the AF mute SW (IC4) disconnects the AF line from the AF AMP.

A portion of FM-demodulated AF signal from the IF IC (IC2) is passed through the noise filter to filter the noise components (approx. 30 kHz signals) only. The filtered noise components are rectified to be converted into the pulse-type signal by noise detector to produce DC voltage corresponding to the noise level ("SQL" signal).

• SQUELCH CIRCUITS (MAIN-RX only)



NOISE SQUELCH DIAGRAM



The "SQL" signal is amplified by noise AMP (IC11), then applied to the CPU (LG: IC1, pin 62). If the CPU interprets that the applied noise level is higher than preset one, the CPU sends the "RMUTE" signal to the AF mute SW (IC4) to cut the RX AF line off.

4-2 TRANSMITTER CIRCUITS

TX AF CIRCUITS

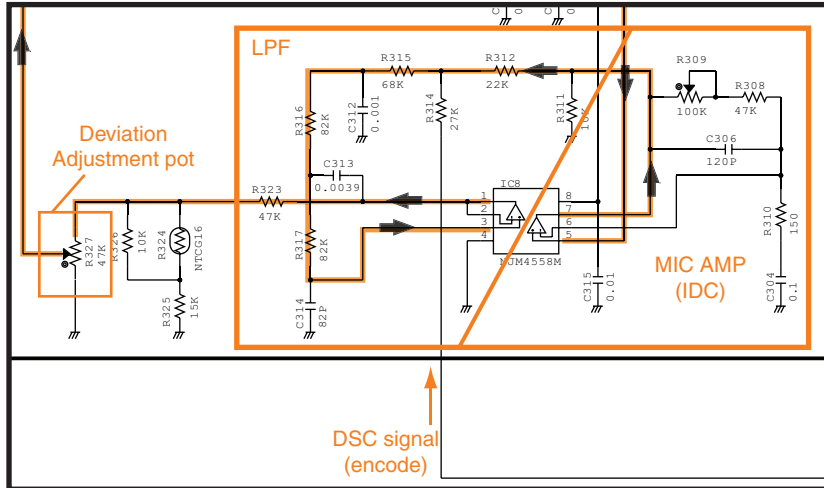
The TX AF circuits consist of microphone amplifier (MIC AMP), MIC mute SW, IDC circuit and AF filters. The IDC (Instant Deviation Control) circuit is an amplifier which adjusts its gain automatically to prevent over deviation.

The audio signals from the microphone (MIC signals) are passed through the MIC mute SW (IC5, pins 1, 2) which cuts off the MIC line in receiving, and applied to the MIC AMP (IC8). The applied MIC signals are amplified within +6 dB/oct of frequency response.

The MIC AMP is included to an IDC circuit which reduces its gain automatically when the high amplitude (=loud voice) signals are applied to the microphone, to prevent over deviation.

The amplitude-limited MIC signals are passed through the splatter filter (LPF) which removes 3 kHz and higher signals, then passed through the deviation adjustment pot (R327). The level-adjusted MIC signals are applied to the modulation circuit of the TX/CH70-RX VCO (Q4, 5, D1-3) as the modulation signals.

• TX AF CIRCUITS



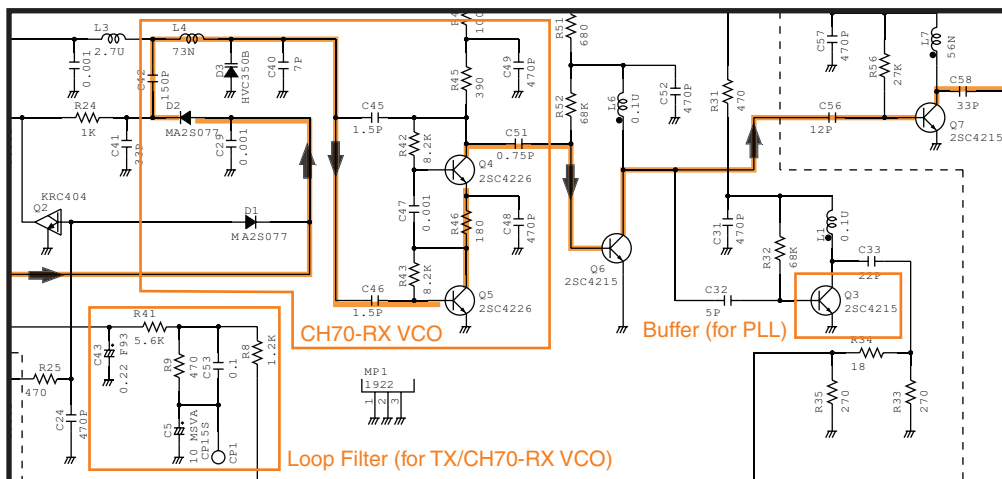
MODULATION CIRCUIT

The modulation circuit FM-modulates the VCO oscillating signal with the MIC signals (=modulation signals) and DSC encoding signals. The amplitude of applied modulation signals vary the capacitance of variable capacitor. Thus the oscillating frequency is shifted by the modulation signals in proportionally to obtain the frequency modulation.

The modulation signals from the deviation adjustment pot (R327) are applied to the variable capacitors (D1, 2) of TX/CH70-RX VCO (Q4, 5, D1-3) for FM modulation.

The modulated VCO output is passed through the buffers (Q6, 7) and LO SW (D7), then applied to the TX AMP.

• MODULATION CIRCUIT



TX AMPS

The TX amplifiers consist the YGR and power amplifiers, and amplify the TX/CH70-RX VCO output to the transmit output power level.

The buffer-amplified VCO output from the TX/CH70-RX VCO is applied to the YGR amplifier (Q10) as the TX signal via LO SW (D7), and amplified to the level need to drive the RF power module (IC3). The amplified TX signal is applied to the RF power module which provides the 25 W of RF output with 10 mW input.

The power-amplified TX signal is passed through the TX power detector, ANT SW and LPF (as a harmonic filter), then applied to the antenna.

APC CIRCUIT

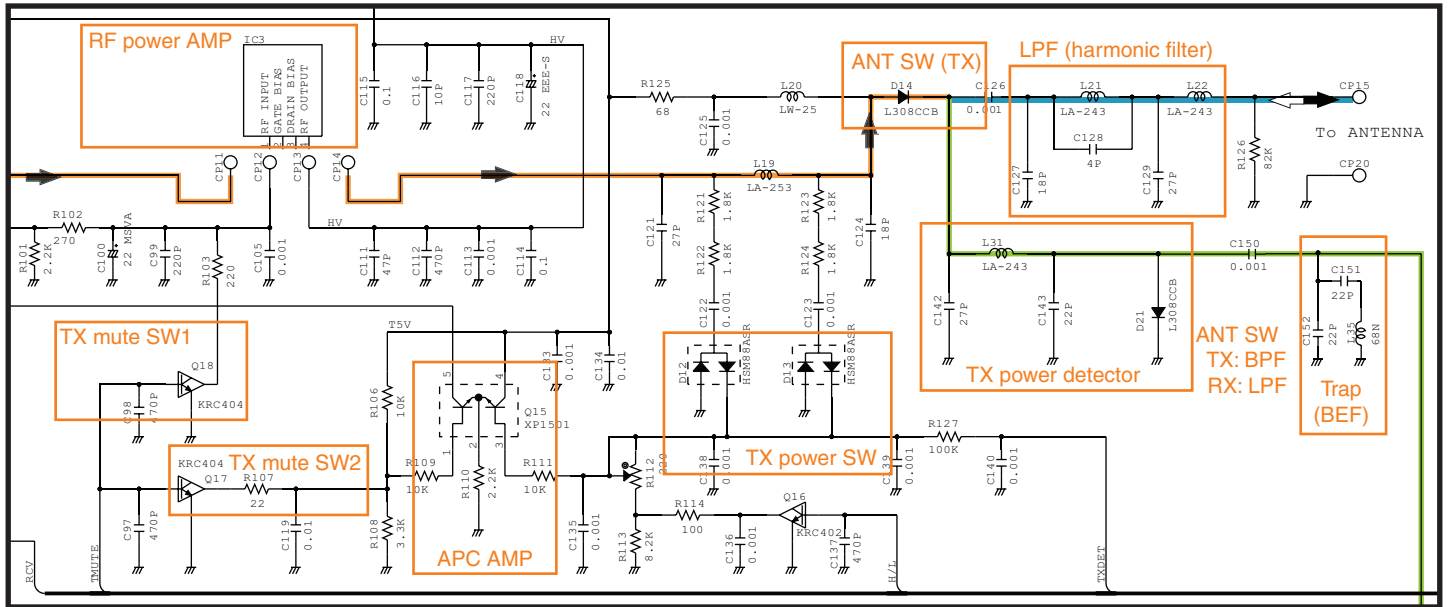
The APC (Automatic Power Control) circuit stabilizes transmit output power to prevent the transition of transmit output power level, which is caused by load mismatching or heat effect, etc. The APC circuit also protects the TX amplifier from the reflected waves caused by load mismatching.

The power detector rectifies a portion of the TX signal and converts it into DC voltage which is in proportion to the transmit output power. The rectified voltage is applied to the base terminal (pin 3) of dual transistor (Q15; as a differential AMP). The TX power setting voltage (divided voltage of T5V) is applied to another base terminal (pin 3) as the reference.

The differential AMP compares the rectified voltage and reference voltage, and the difference of these voltages is appeared on the corrector terminal (pin 5) to drives APC driver (Q14). The APC driver controls the gate bias of RF power module to reduce/increase the gain for stable TX output power.

The TX muting is carried out by applying voltage ("TMUTE") to the TX mute SWs (Q17, 18). When the PLL unlock, etc. is occurred, "TMUTE" becomes to "High" and TX mute SWs are ON to stop supplying voltage to the RF power AMP.

• TX AMPS AND APC CIRCUIT



4-3 FREQUENCY SYNTHESIZER CIRCUITS VCOs

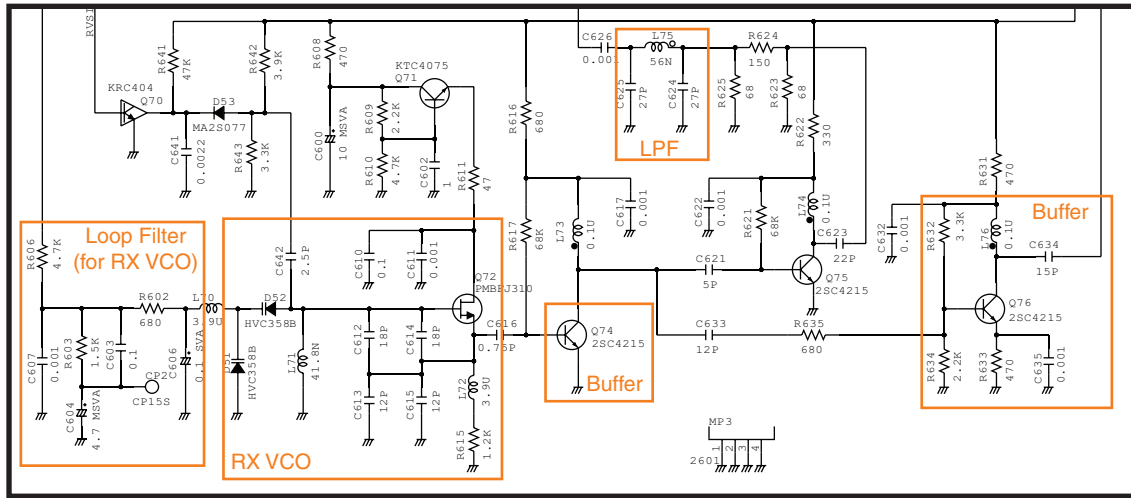
A VCO is an oscillator whose oscillating frequency is controlled by the applied voltage. This transceiver has two VCOs; RX VCO and TX/CH70-RX VCO. The RX VCO generates the 1st LO signals for MAIN-RX, TX/CH70-RX VCO generates TX signal in transmitting and 1st LO signals for CH70-RX in receiving.

• RX VCO (Q72, D51-53)

The RX VCO oscillates 134.300 to 141.725 MHz LO signals for MAIN-RX. The generated 1st LO signals are applied to the 1st mixer (Q22) via the buffer AMPs (Q74, 76), LPF (harmonic filter; L12, 13, C77).

A portion of the VCO output is applied to the PLL IC (IC1) via the buffer (Q74, 75) and LPF (L75, C624, 625).

• RX VCO



• TX/CH70-RX VCO (Q4, 5, D1-3)

<While transmitting>

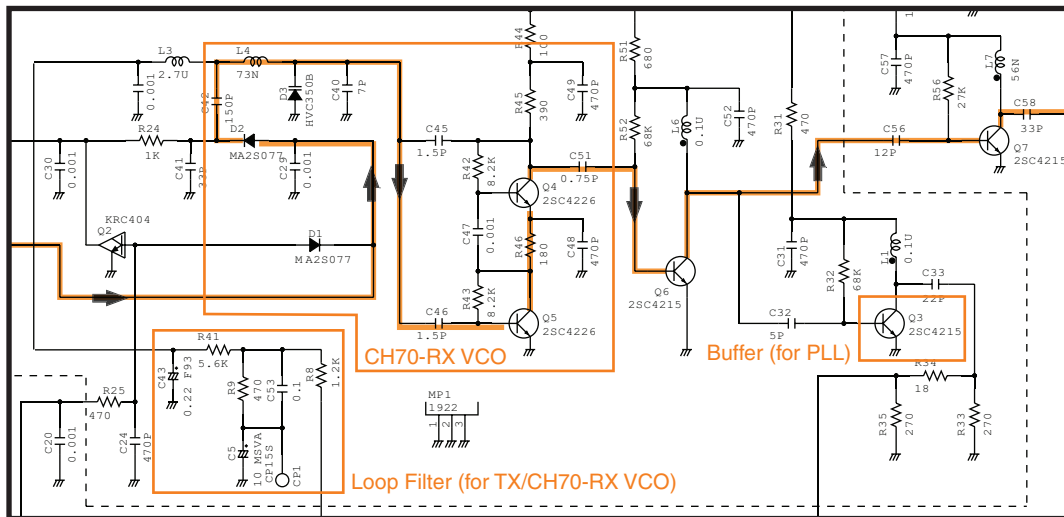
The TX/CH70-RX VCO oscillates 156.000 to 161.450 MHz transmit signals. The generated TX signal is applied to the YGR amplifier (Q10) via the buffer AMPs (Q6, 7) and LO SW (D7).

<While receiving>

The TX/CH70-RX VCO oscillates 125.650 MHz 1st LO signal for CH70-RX (156.525 MHz). The VCO output is applied to the 1st mixer (Q62) via the buffer AMPs (Q6, 7) and LO SW (D8).

A portion of the VCO output is applied to the PLL IC (IC1) via the buffer (Q6, 3) and LPF (L2, C34, 35).

• TX/CH70RX VCO



PLL (Phase Locked Loop) CIRCUIT

The PLL circuit provides stable oscillation for both of the TX and 1st LO frequencies. By comparing fed back VCO output and reference frequency signals, the PLL corrects the difference of these frequencies.

• RX VCO LOOP

A portion of RX VCO output is applied to the PLL IC (IC1, pin 3) via buffers (Q74, 75) and LPF (L75, C624, 625).

• TX/CH70-RX VCO LOOP

A portion of TX/CH70-RX VCO output is applied to the PLL IC (IC1, pin 17) via buffers (Q6, 3) and LPF (L2, C34, 35).

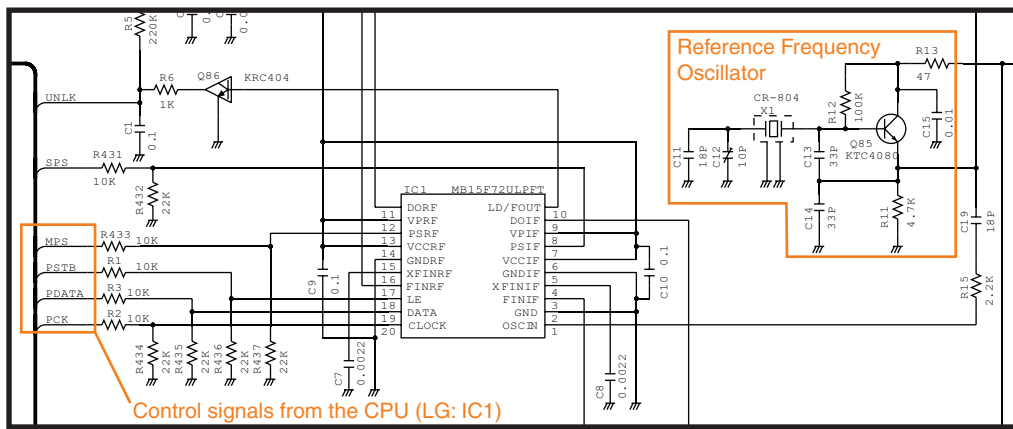
The applied VCO output signal is divided according to the divide ratio which is included in the serial data from the CPU (LG: IC1), at the prescaler and programmable divider. In the same way, the reference frequency signal from the reference frequency oscillator is applied to the PLL IC and divided so that these two applied signals are the same frequency.

The frequency-matched signals (VCO output and the reference frequency signals) are applied to the phase comparator and phase-compare. The resulted phase difference is detected as a pulse-type signal, and level-adjusted at the charge pump then output. The output pulse-type signal is passed through the loop filter to be converted into the DC voltage (=Lock Voltage).

The capacitance of variable capacitors of each VCOs changes corresponding to the applied lock voltage. This causes the change of the VCO oscillating frequency to keep the VCO frequency constant.

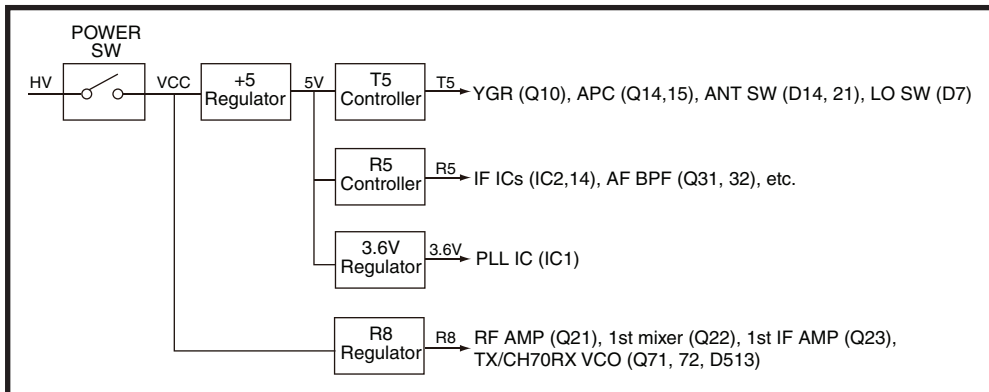
When the oscillation frequency drifts, its phase changes from that of the reference frequency, causing a lock voltage change to compensate for the drift in the VCO oscillating frequency.

• PLL



4-4 VOLTAGE LINES

Voltage from the power supply is routed to the whole of transceiver through the regulators and switches.



4-5 CPU (LG; IC1) PORT ALLOCATION

Pin No.	LINE	DESCRIPTION	I/O
3	PSTB	Strobe signal to the PLL IC (M: IC1).	O
4	PCK	Clock signal to the PLL IC (M: IC1).	O
5	PDATA	Serial data to the PLL IC (M: IC1).	O
6	PSPS	Power control signal to the PLL IC (M: IC1) for RX VCO loop. (GND level)	O
7	PMPS	Power control signal to the PLL IC (M: IC1) for TX/CH70-RX VCO loop. (GND level)	O
12	PTT	[PTT] key input. (pulled up)	I
21–23	DIM1–3	Brightness (Dimmer) control signals to the LED driver (LG: Q1–3).	O
30	DOWN	[DWN] key input. (pulled up)	I
31	UP	[UP] key input. (pulled up)	I
32	CH16	[CH16] key input. (pulled up)	I
33	SCAN	[SCAN] key input. (pulled up)	I
34	CH	[CH] key input. (pulled up)	I
35	CLR	[CLR] key input. (pulled up)	I
36	MENU	[MENU] key input. (pulled up)	I
37	ENT	[ENT] key input. (pulled up)	I
38	LCDCLK	Clock signal (25 kHz) to the LCD driver (LG: IC8).	O
39	H/L	TX power control signal to the TX power SW (M: Q16). "H"= When the TX power is 'high.'	O
41	DSDEC	Decoded DSC signal from the DSC decoder (M: IC15).	I
42	UNLK	PLL unlock signal from the PLL IC (M: IC1). "L"= When the PLL is unlocked.	I
53	NMRXD	NMEA data (4800 bps) from photo-coupler (LG: IC5).	I
54	ECK	Clock signal to the EEPROM (LG: IC3).	O
55	EDATA	Data I/O port for EEPROM (LG: IC3).	I/O
57	DS/BPFV	<ul style="list-style-type: none"> • RX (MAIN-RX) Tuning voltage to the tuned BPF (M: D26–28) via buffer (LG: IC2). • TX DSC encode signal to the LPF (M: IC8). 	O
58	BEEP	Beep sounds (500, 750, 1k, 1.3k and 2.2 kHz square waves).	O
61	SQLV	[SQL] dial input. (divided voltage of 5V line)	I
62	SQL	Noise level from the IF IC (M: IC2) via noise AMP (M: IC11).	I
63	LBAT	Power supply voltage detect (voltage of VCC line divided by R36 and R37(LG)).	I
64	TXDET	TX power detect (rectified voltage from TX power detector (M: D12, 13)).	I
65	KEYM	Key detect signal (divided voltage) from the HM-164.	I
70	TMUTE	TX mute signal to the TX mute controller (M: Q17, 18). "H"=TX mute.	O
71	MICM	MIC mute (MIC line disconnect) signal to the MIC mute SW (M: IC5). "L"=MIC mute (=in RX).	O
72	SEND	Power line control signal to the T5 line controller (M: Q57, 58). "H"=T5 line is active (=in TX).	O
73	RMUTE	RX AF mute control signal to the AF mute SW (M: IC4). "L"=AF mute (squelch close).	O
74	RCV	Power line control signal to the R5 line controller (M: Q53, 54). "H"=R5 line is active (=in RX).	O
78	LCE	Strobe signal to the LCD driver (LG: IC8).	O
79	LSCL	Clock signal to the LCD driver (LG: IC8).	O
80	LSI	Serial data to the LCD driver (LG: IC8).	O

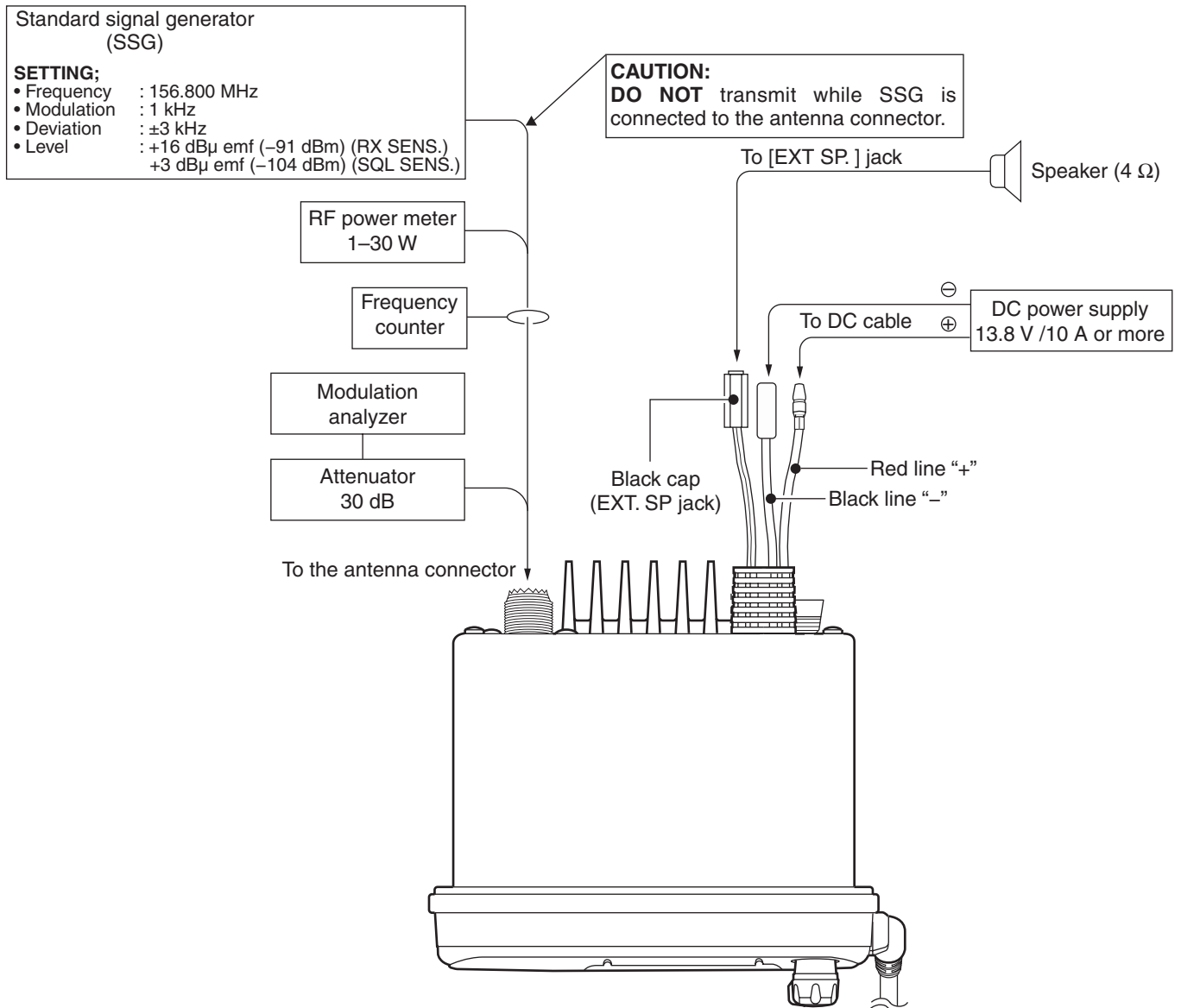
SECTION 5 ADJUSTMENT PROCEDURE

5-1 PREPARATION

REQUIRED INSTRUMENTS

INSTRUMENT	SPECIFICATION	INSTRUMENT	SPECIFICATION
Power Supply	Output voltage : 13.8 V DC Current capacity : More than 10 A	Digital Voltmeter	Input impedance : More than 50 k Ω Measuring range : 0.1–10V
RF Power Meter (terminated type)	Measuring range : 0.1–30 W Frequency range : 100–300 MHz Impedance : 50 Ω SWR : Less than 1.2 : 1	Standard Signal Generator (SSG)	Frequency range : 0.1–300 MHz Output level : 0.1 mV to 32 mV (–127 to –17 dBm)
Frequency Counter	Frequency range : 0.1–300 MHz Frequency accuracy : ± 1 ppm or better Input level : Less than 1 mW	AC Millivoltmeter	Measuring range : 10 mV to 10 V
Modulation Analyzer	Frequency range : 30–300 MHz Measuring range : 0 to ± 10 kHz	External Speaker	Input impedance : 4 Ω Capacity : More than 3 W
Audio Generator	Frequency range : 300–3000 Hz Output level : 1–500 mV	Attenuator	Power attenuation : 30 dB Capacity : More than 30 W

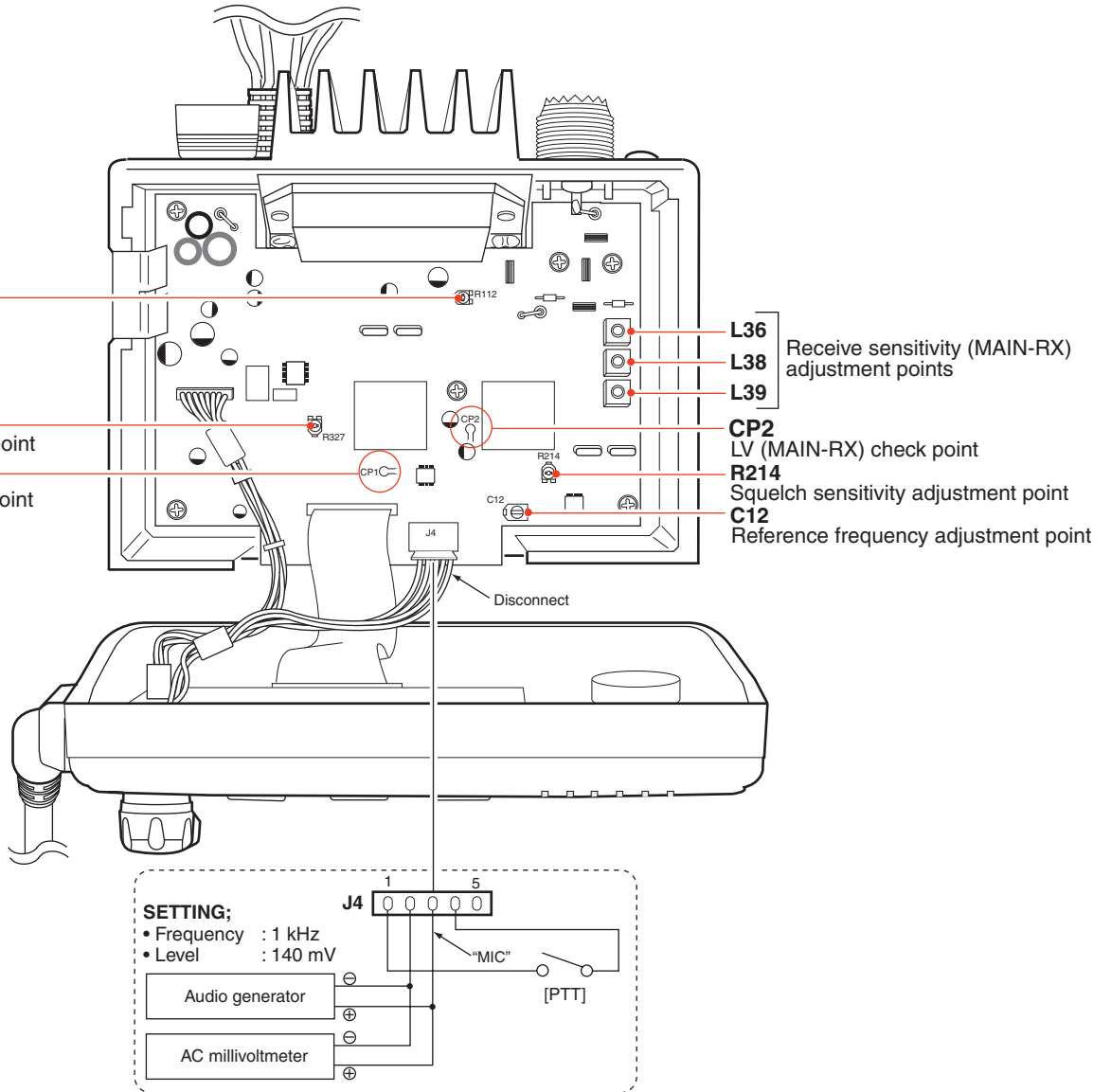
CONNECTION



5-2 ADJUSTMENT (See the next page for the location of check /adjust points.)

ADJUSTMENT	ADJUSTMENT CONDITION	OPERATION	ADJUSTMENT POINT	VALUE
LOCK VOLTAGE VERIFICATION (RX)	1	• Channel : 16 • Receiving	Connect a Digital voltmeter to the CP2 (MAIN UNIT).	– 1.1–2.1 V (Verify)
	2	• Channel : 16 • Receiving	Connect a Digital voltmeter to the CP1 (MAIN UNIT).	– 1.0–2.0 V (Verify)
(TX)	3	• Channel : 16 • Output power: Low • Transmitting	1) Connect an RF Power Meter to the antenna connector. 2) Connect a Digital voltmeter to the CP1 (MAIN UNIT).	– 1.0–2.0 V (Verify)
REFERENCE FREQUENCY	1	• Channel : 16 • Transmitting	Loose-couple a Frequency Counter to the antenna connector.	C12 (MAIN UNIT) 156.800 MHz
TRANSMIT OUTPUT POWER	1	• Channel : 16 • Output power: High • Transmitting	Connect an RF Power Meter to the antenna connector.	R112 (MAIN UNIT) 23–23.5 W
DEVIATION (PRESET)	1	• Channel : 16 • Output power: Low • Transmitting	Set the R309 (MAIN UNIT) to center position.	R309 (MAIN UNIT) Center position
(ADJUST)	2		1) Disconnect the cable from J4 (MAIN UNIT). 2) Connect an Audio Generator and AC millivoltmeter to the "MIC" line, and set as; • Frequency : 1 kHz • Level : 140 mV 3) Connect a Modulation Analyzer to the antenna connector through an attenuator.	R327 (MAIN UNIT) ±4.25–4.35 kHz
RECEIVE SENSITIVITY (MAIN-RX)	1	• Channel : 16 • Receiving	1) Connect a Standard Signal Generator to the antenna, and set as; • Frequency : 156.800 MHz • Modulation : 1 kHz • Deviation : ±3 kHz • Level : +16 dBμ emf (–91 dBm) 2) Connect a 4 Ω speaker to the [EXT. SP] jack. 3) Connect a Digital voltmeter to the CP3 (MAIN UNIT).	L38→L36→L39 (in sequence, repeatedly) Maximum voltage
SQUELCH (MAIN-RX) -Preparation-	1	• Channel : 16 • [SQL] : Max. CW • Receiving	1) Connect a Standard Signal Generator to the antenna, and set as; • Frequency : 156.800 MHz • Modulation : 1 kHz • Deviation : ±3 kHz • Level : +3 dBμ emf (–104 dBm) 2) Connect a 4 Ω speaker to the [EXT. SP] jack.	– –
-Adjust-	2		1) Turn the R214 (MAIN UNIT) clockwise to close the squelch. (If the squelch does not close, turn the SSG's output OFF, then turn it ON again.) 2) Set the R214 to the point where the squelch just opens.	R214 (MAIN UNIT) Squelch open

• ADJUST&CHECK POINTS



SECTION 6

PARTS LIST

[LOGIC UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
IC1	1140014500	S.IC uPD78F1153GK-GAK-AX	B	50.6/30.7
IC2	1110006380	S.IC LM2904PWR	B	35.3/22.4
IC3	1140008650	S.IC HN58X2464TI	B	42.2/18.2
IC4	1110005771	S.IC S-80942CNMC-G9CT2G	B	83.5/27.8
IC5	1170000352	S.IC PC357N6J000F	B	22.3/43.6
IC8	1130014050	S.IC NJU6535FH1	B	38.6/50.6
Q1	1520000840	S.TR KTA1664Y-RTF/P	B	78/19.9
Q2	1530003900	S.TR KTC4075 BL-RTK/P	B	73.7/18
Q3	1530003900	S.TR KTC4075 BL-RTK/P	B	73.7/21.8
Q4	1590000660	S.TR DTC144TU T106	B	22.1/40
Q5	1590003670	S.TR KRA304-RTK/P	B	30.9/37.4
D1	1750001320	S.DIO KDS4148U RTK/P	B	24.3/39.8
D2	1750001320	S.DIO KDS4148U RTK/P	B	17/43.6
X1	6050012471	S.XTL CR-836A (SMD-49/9.8304 MHz)	B	66.8/37.8
R22	7030003680	S.RES ERJ3GEYJ 104 V (100 k)	B	37.8/23.5
R23	7030003800	S.RES ERJ3GEYJ 105 V (1 M)	B	39.1/23.5
R24	7030003500	S.RES ERJ3GEYJ 332 V (3.3 k)	B	35/26.9
R25	7030005250	S.RES ERJ3GEYF 103 V (10 k)	B	39.1/20.6
R26	7030003540	S.RES ERJ3GEYJ 682 V (6.8 k)	B	37.8/20.6
R31	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	41.2/24.1
R32	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	41.2/22.8
R35	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	34.7/32.9
R36	7030004820	S.RES ERJ3GEYF 473 V (47 k)	B	40.1/38.3
R37	7030010020	S.RES ERJ2RKF 154 X (150 k)	B	38.8/36.8
R41	7030003680	S.RES ERJ3GEYJ 104 V (100 k)	B	63.2/30.9
R42	7030003600	S.RES ERJ3GEYJ 223 V (22 k)	B	83/37.6
R43	7030003480	S.RES ERJ3GEYJ 222 V (2.2 k)	B	83/41.4
R44	7030003640	S.RES ERJ3GEYJ 473 V (47 k)	B	88.8/25.8
R45	7030003640	S.RES ERJ3GEYJ 473 V (47 k)	B	86.1/26.7
R46	7030003320	S.RES ERJ3GEYJ 101 V (100)	B	58.2/37.1
R47	7030003280	S.RES ERJ3GEYJ 470 V (47)	B	60.8/26.9
R48	7030003280	S.RES ERJ3GEYJ 470 V (47)	B	60.8/25.6
R49	7030003280	S.RES ERJ3GEYJ 470 V (47)	B	60.8/24.3
R50	7030003200	S.RES ERJ3GEYJ 100 V (10)	B	63.9/32.8
R51	7030003230	S.RES ERJ3GEYJ 180 V (18)	B	75.9/23.8
R52	7030003230	S.RES ERJ3GEYJ 180 V (18)	B	77.2/23.8
R54	7030003530	S.RES ERJ3GEYJ 562 V (5.6 k)	B	64/27.3
R55	7030003500	S.RES ERJ3GEYJ 332 V (3.3 k)	B	64/26
R60	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	64/24.7
R61	7030003510	S.RES ERJ3GEYJ 392 V (3.9 k)	B	71.4/18.1
R62	7030003440	S.RES ERJ3GEYJ 102 V (1 k)	B	72.8/24.5
R63	7030003550	S.RES ERJ3GEYJ 822 V (8.2 k)	B	73.6/19.9
R64	7030003460	S.RES ERJ3GEYJ 152 V (1.5 k)	B	71.5/24.5
R71	7030003430	S.RES ERJ3GEYJ 821 V (820)	B	85.3/8.1
R72	7030003430	S.RES ERJ3GEYJ 821 V (820)	B	39.3/8.2
R73	7030003430	S.RES ERJ3GEYJ 821 V (820)	B	11.4/8.7
R74	7030000320	S.RES MCR10EZHZ 330 (331)	B	86.6/33.7
R75	7030000320	S.RES MCR10EZHZ 330 (331)	B	11.8/32.8
R76	7030000320	S.RES MCR10EZHZ 330 (331)	B	12.2/50.2
R81	7030003680	S.RES ERJ3GEYJ 104 V (100 k)	B	75.6/14.4
R82	7030003520	S.RES ERJ3GEYJ 472 V (4.7 k)	B	26.3/37.4
R83	7030003520	S.RES ERJ3GEYJ 472 V (4.7 k)	B	23.5/37.4
R84	7030003410	S.RES ERJ3GEYJ 561 V (560)	B	21.4/36.9
R85	7030003400	S.RES ERJ3GEYJ 471 V (470)	B	19.5/40.8
R86	7030003520	S.RES ERJ3GEYJ 472 V (4.7 k)	B	27.9/45.4
R87	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	81.7/41.4
R88	7030003560	S.RES ERJ3GEYJ 103 V (10 k)	B	81.7/44.2
R92	7030003680	S.RES ERJ3GEYJ 104 V (100 k)	B	46.3/42.1
R98	7030003340	S.RES ERJ3GEYJ 151 V (150)	B	34.8/37.8
C1	4510008540	S.ELE EEE1CA100SR	B	79.4/48.2
C2	4030016930	S.CER ECJ0EB1A104K	B	77.8/51.3
C3	4030016790	S.CER ECJ0EB1C103K	B	77.8/52.3
C4	4030016930	S.CER ECJ0EB1A104K	B	30.4/39.6
C21	4030016930	S.CER ECJ0EB1A104K	B	37.5/26.2
C31	4030016790	S.CER ECJ0EB1C103K	B	44/21.9
C32	4030016930	S.CER ECJ0EB1A104K	B	44.9/21.9
C33	4030016930	S.CER ECJ0EB1A104K	B	80.5/41.7
C34	4030016930	S.CER ECJ0EB1A104K	B	40.3/36.5
C41	4030016930	S.CER ECJ0EB1A104K	B	63.6/29.8
C42	4030016790	S.CER ECJ0EB1C103K	B	87.4/25.8
C44	4030017030	S.CER ECJ0EB1A273K	B	82.7/30
C45	4030017650	S.CER ECJ0EC1H270J	B	59.4/37.7
C46	4030017410	S.CER ECJ0EC1H240J	B	60.3/36
C47	4030017490	S.CER C1608 JB 1A 105K-T	B	62.7/33.7
C48	4030017460	S.CER ECJ0EB1E102K	B	61.6/32.9
C49	4030017490	S.CER C1608 JB 1A 105K-T	B	60.6/34.5
C51	4510008540	S.ELE EEE1CA100SR	B	83/23.6
C52	4030017460	S.CER ECJ0EB1E102K	B	74.1/24.5
C81	4030016930	S.CER ECJ0EB1A104K	B	69.1/12.2
C82	4030017780	S.CER ECJ0EB1E472K	B	15.4/43.6
C83	4030016790	S.CER ECJ0EB1C103K	B	26.3/41
C84	4030016930	S.CER ECJ0EB1A104K	B	26.3/40
C90	4030017420	S.CER ECJ0EC1H470J	B	28.6/48.8
C91	4030017420	S.CER ECJ0EC1H470J	B	28.9/47.6
C92	4030017420	S.CER ECJ0EC1H470J	B	28.9/46.6
C93	4030017400	S.CER ECJ0EC1H220J	B	30.8/46.6
C94	4030016930	S.CER ECJ0EB1A104K	B	35.5/40.4
C95	4030016930	S.CER ECJ0EB1A104K	B	32.7/40.5

[LOGIC UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C96	4030016930	S.CER ECJ0EB1A104K	B	32.4/44.2
C97	4030016790	S.CER ECJ0EB1C103K	B	35.2/39
C98	4030017490	S.CER C1608 JB 1A 105K-T	B	34.8/36.5
C99	4030017490	S.CER C1608 JB 1A 105K-T	B	34.8/35.2
C101	4030016930	S.CER ECJ0EB1A104K	B	77.5/15.1
C102	4510008540	S.ELE EEE1CA100SR	B	72.5/11.6
C104	4030017460	S.CER ECJ0EB1E102K	B	10.9/43.5
C105	4030017460	S.CER ECJ0EB1E102K	B	7.7/52.7
C106	4030016790	S.CER ECJ0EB1C103K	B	68.1/46.1
C107	4030016790	S.CER ECJ0EB1C103K	B	68.1/45.1
C108	4030017460	S.CER ECJ0EB1E102K	B	73.7/45.2
C109	4030017460	S.CER ECJ0EB1E102K	B	74.7/45.2
C110	4030017460	S.CER ECJ0EB1E102K	B	63.8/45.2
C122	4030016930	S.CER ECJ0EB1A104K	B	40.2/27.9
C123	4030017460	S.CER ECJ0EB1E102K	B	39.2/27.9
J1	6510022471	S.CNR 40FLT-SM2-TB (LF) (SN)	B	65.4/49
J3	6510018971	S.CNR B4B-PH-SM4-TB (LF) (SN)	B	21.9/51.2
DS1	5030002940	LCD SDT-DT161-HP-O <VKH>	T	69.9/11.8
DS2	5040003310	S.LED L-C191KYCT <SHO>	T	82.7/4.2
DS3	5040003310	S.LED L-C191KYCT <SHO>	T	54.6/11.8
DS4	5040003310	S.LED L-C191KYCT <SHO>	T	39.3/11.8
DS5	5040003310	S.LED L-C191KYCT <SHO>	T	24/11.8
DS6	5040003310	S.LED L-C191KYCT <SHO>	T	10.2/11.8
DS7	5040003310	S.LED L-C191KYCT <SHO>	T	85.7/50.9
DS8	5040003230	S.LED RY-SP110UHY24-5M <VKH>	T	85.7/42.2
DS9	5040003230	S.LED RY-SP110UHY24-5M <VKH>	T	85.7/33.5
DS10	5040003230	S.LED RY-SP110UHY24-5M <VKH>	T	10.5/33.5
DS11	5040003230	S.LED RY-SP110UHY24-5M <VKH>	T	10.5/42.2
DS12	5040003230	S.LED RY-SP110UHY24-5M <VKH>	T	10.5/50.9
DS13	5040003230	S.LED RY-SP110UHY24-5M <VKH>	T	10.5/50.9
EP2	8930066470	LCT SRCN-2852-SP-N-W		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
C550	4030017460	S.CER ECJ0EB1E102K	T	31.9/41.8
C551	4030016930	S.CER ECJ0EB1A104K	T	36.9/54.8
C552	4030016930	S.CER ECJ0EB1A104K	T	38.2/55.2
C565	4030017660	S.CER ECJ0EC1H330J	T	29.5/56.6
C566	4030017400	S.CER ECJ0EC1H220J	T	31.9/53.6
C571	4030016930	S.CER ECJ0EB1A104K	T	25/20.4
C572	4030017790	S.CER ECJ0EB1E682K	T	22.7/21.8
C573	4030016790	S.CER ECJ0EB1C103K	T	19.9/16.1
C574	4030016970	S.CER ECJ0EB1C223K	T	19.8/18.2
C575	4030017760	S.CER ECJ0EB1H222K	T	19.9/20
C576	4030017790	S.CER ECJ0EB1E682K	T	17.3/20
C581	4030016790	S.CER ECJ0EB1C103K	T	14.7/17.7
C582	4030018890	S.CER ECJ0EB0J224K	T	14.7/20
C584	4030016930	S.CER ECJ0EB1A104K	T	12.6/13.9
C585	4510008540	S.ELE EEE1CA100SR	T	15.5/13.3
C586	4310000420	MLR 50 F2D 333J		
C587	4030017760	S.CER ECJ0EB1H222K	T	3.7/23.4
C588	4030016930	S.CER ECJ0EB1A104K	T	3.7/22.1
C589	4030016790	S.CER ECJ0EB1C103K	T	3.9/19.5
C600	4550006250	S.TAN TEESVA 1A 106M8R	T	82.3/34.5
C602	4030017490	S.CER C1608 JB 1A 105K-T	T	87.3/36.1
C603	4030016930	S.CER ECJ0EB1A104K	T	73.7/32.3
C604	4550005980	S.TAN TEESVA 1A 475M8R	T	76/31.4
C606	4550000530	S.TAN TEESVA 1V 104M8R	T	82/43
C607	4030017460	S.CER ECJ0EB1E102K	T	80.1/44.8
C610	4030016930	S.CER ECJ0EB1A104K	T	92.8/42.6
C611	4030017460	S.CER ECJ0EB1E102K	T	91.9/42.6
C612	4030017390	S.CER ECJ0EC1H180J	T	89/45.9
C613	4030017630	S.CER ECJ0EC1H120J	T	89/44.2
C614	4030017390	S.CER ECJ0EC1H180J	T	90/45.9
C615	4030017630	S.CER ECJ0EC1H120J	T	90/44.2
C616	4030017540	S.CER ECJ0EC1HR75B	T	95.9/40.8
C617	4030017460	S.CER ECJ0EB1E102K	T	91.1/41.2
C621	4030017380	S.CER ECJ0EC1H050B	T	89.4/35.5
C622	4030017460	S.CER ECJ0EB1E102K	T	88.3/33.5
C623	4030017400	S.CER ECJ0EC1H220J	T	84.7/30.9
C624	4030017650	S.CER ECJ0EC1H270J	T	71.5/21.4
C625	4030017650	S.CER ECJ0EC1H270J	T	71.7/20.2
C626	4030017460	S.CER ECJ0EB1E102K	T	70.4/19.8
C632	4030017460	S.CER ECJ0EB1E102K	T	92.9/31.9
C633	4030017630	S.CER ECJ0EC1H120J	T	92.9/38.1
C634	4030017640	S.CER ECJ0EC1H150J	T	94.4/34.4
C635	4030017460	S.CER ECJ0EB1E102K	T	91.1/31.9
C641	4030017760	S.CER ECJ0EB1H222K	T	83.9/39.6
C642	4030017560	S.CER ECJ0EC1H2R5B	T	83.9/41.4
C646	4030017440	S.CER ECJ0EC1H221J	T	17.9/68.2
C647	4030017440	S.CER ECJ0EC1H221J	T	7.3/51
C648	4030016790	S.CER ECJ0EB1C103K	T	20.3/50.5
C649	4030016790	S.CER ECJ0EB1C103K	T	16.8/50.5
J4	6510024601	CNR S5B-PH-K-S (LF) (SN)		
J6	6510022471	S.CNR 40FLT-SM2-TB (LF) (SN)	T	36/12
W1	7120000470	JMP ERDS2T0		
W2	8900012401	CBL OPC-1264A		
W3	8900016880	CBL OPC-1768 (P0.5,N40,L65)		
EP341	6910014690	S.BEA MPZ1608S221A-T	T	9.9/63.9
EP342	6910014690	S.BEA MPZ1608S221A-T	T	10/67
EP401	6910018270	S.BEA MMZ1608Y301CT	T	46.8/3.4
EP402	6910018270	S.BEA MMZ1608Y301CT	T	21.3/37.4
EP403	6910018270	S.BEA MMZ1608Y301CT	T	5/72.8
EP404	6910018270	S.BEA MMZ1608Y301CT	T	53.3/11.1
EP405	6910018270	S.BEA MMZ1608Y301CT	T	57.3/11.1

[VR UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
R1	7210003140	VAR TP96N97N-15SK-10KA-2685		
J1	6510009381	CNR B5B-ZR (LF) (SN)		

[SQL UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.	H/V LOCATION
R1	7210003150	VAR TP96N97-15SK-10KB-2685		
J1	6510009471	CNR S3B-ZR (LF) (SN)		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

SECTION 7

MECHANICAL PARTS

[FRONT UNIT]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
R1*	7070001220	3.9 ERX2SJ	1
MC1	0800009280	HM-164B (#01)	1
SP1	2510001350	045P0806-7-1	1
W1*	7120000470	ERDS2T0	1
MP1	8210024510	3054 FRONT PANEL (Incl. MC1, MP2-4, MP6-9)	1
MP2	8310070050	3054 WINDOW PLATE	1
MP3	8930073320	3054 SP NET	1
MP4	8930064020	2807 KEYBOARD	1
MP5	8610011370	KNOB N-312	2
MP6	8110009290	3054 D-COVER	1
MP7	8930073330	3054 SHAFT	1
MP8	8930073300	3054 SHAFT ANGLE	1
MP9	8930073340	3054 SPRING	1
MP10	8930073310	3054 F-BUSH PLATE	1
MP11	8930055841	2490 EARTH SPRING-1	1
MP12	8930052280	O-ring (AC)	2
MP13	8810008661	Screw BT B0 3X8 NI-ZC3 (BT)	4

[CHASSIS PARTS]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
J1	6510004880	MR-DSE-01	1
W1	8900016870	OPC-1765	1
MP1	8510018680	3054 CASE (Incl. MP2, 4)	1
MP2	8830003160	Ultrasert UD-45058-CD NI	2
MP3	8930049040	Insulation sheet (FQ)	1
MP4	8930073350	3054 SHEET	1
MP5	8930064040	2807 R-PACKING	1
MP6	8930064030	2807 F-PACKING	1
MP7	8930073290	3054 R-BUSH PLATE	1
MP8	8930052440	2345 B-IC CLIP	1
MP9	8510018620	3054 MODULE PLATE	1
MP10	8510018630	3054 MODULE COVER	1
MP11	8010020970	3054 CHASSIS (Incl. J1, W1, MP7, 12, 13, 18)	1
MP12	8930073360	3054 ANT SEAL	1
MP13	8810010730	Screw BiH M3X6 SUS	2
MP14	8810009071	Screw PH M2.6X8 ZK3	2
MP15	8810008661	Screw BT B0 3X8 NI-ZC3 (BT)	6
MP16	8810004540	Screw BiH M3X8 SUS	4
MP17	8810004700	Screw BT A0 3X16 SUS	4
MP18	8950000180	Cable tie -80	1

[LOGIC UNIT]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
DS1	5030002940	SDT-DT161-HP-O	1
EP2	8930066470	SRCN-2852-SP-N-W	1
MP1	8930066230	2852 LCD HOLDER	1
MP2	8210020990	2807 REFLECTOR	1
MP3	8930066400	2852 LCD FILTER	1
MP4	8930066390	2852 LCD SHEET	1

*: Refer to SECTION 8 "BOARD LAYOUTS."

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
J4*	6510024601	S5B-PH-K-S (LF) (SN)	1
J6*	6510022471	40FLT-SM2-TB (LF) (SN)	1
W1*	7120000470	ERDS2T0	1
W2	8900012401	OPC-1264A	1
W3	8900016880	OPC-1768	1
MP1*	8510011111	1922 VCO CASE-1	1
MP2	8510011101	1922 VCO COVER-1	1
MP3*	8510014940	2601 VCO CASE	1
MP4	8510014950	2601 VCO COVER	1
MP5*	8930005320	Filter spacer	2
MP6*	8930005320	Filter spacer	2

[SQL UNIT]

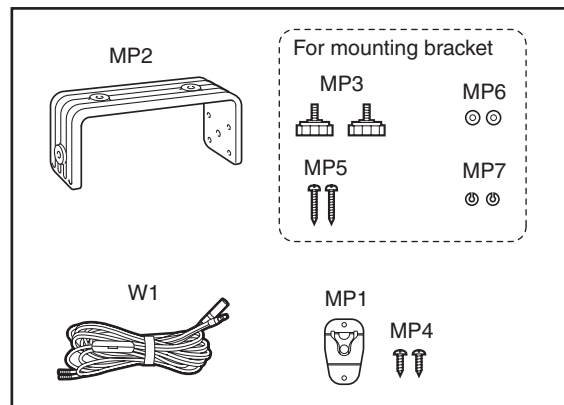
REF NO.	ORDER NO.	DESCRIPTION	QTY.
R1	7210003150	TP96N97-15SK-10KB-2685	1
J1*	6510009471	S3B-ZR (LF) (SN)	1

[VR UNIT]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
R1	7210003140	TP96N97N-15SK-10KA-2685	1
J1*	6510009381	B5B-ZR (LF) (SN)	1

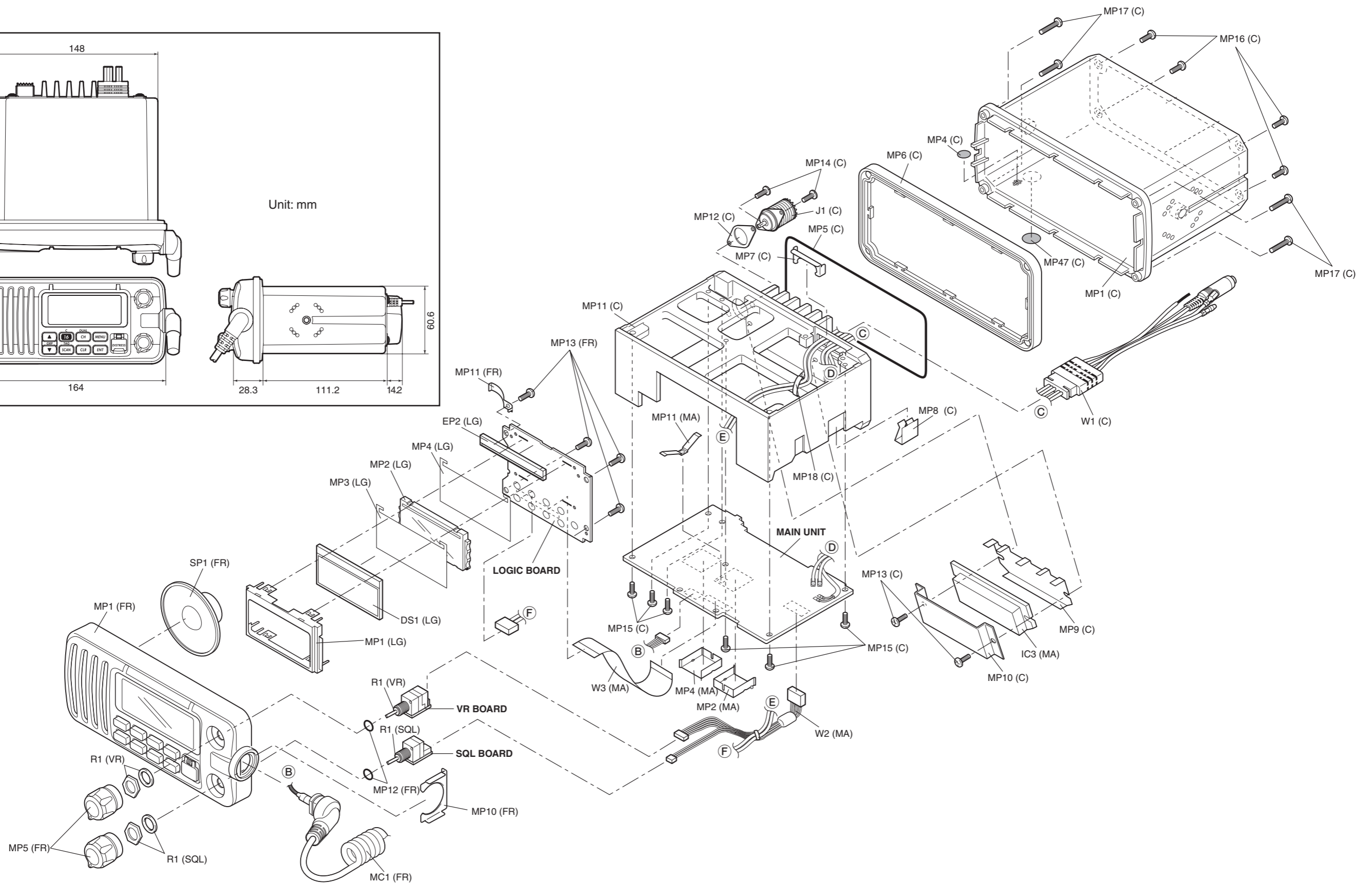
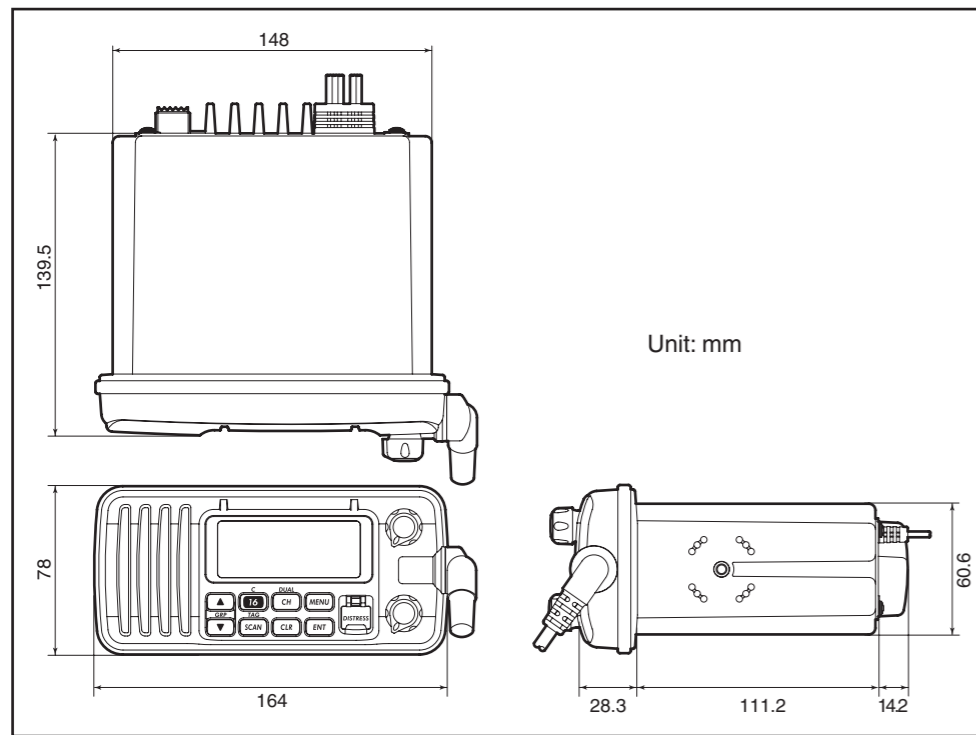
[ACCESSORIES]

REF NO.	ORDER NO.	DESCRIPTION	QTY.
W1	8900009041	OPC-891A	1
MP1	8950007250	3054 MIC HANGER	1
MP2	8010020950	3054 MOBILE BRACKET	1
MP3	8820001460	3054 KNOB BOLT	2
MP4	8810010710	Screw BT A0 3X16 SUS	2
MP5	8810010720	Screw BT A0 5X20 SUS	2
MP6	8850002850	PLAIN WASHER M5 SUS	2
MP7	8850002860	SPRING WASHER M5 SUS	2



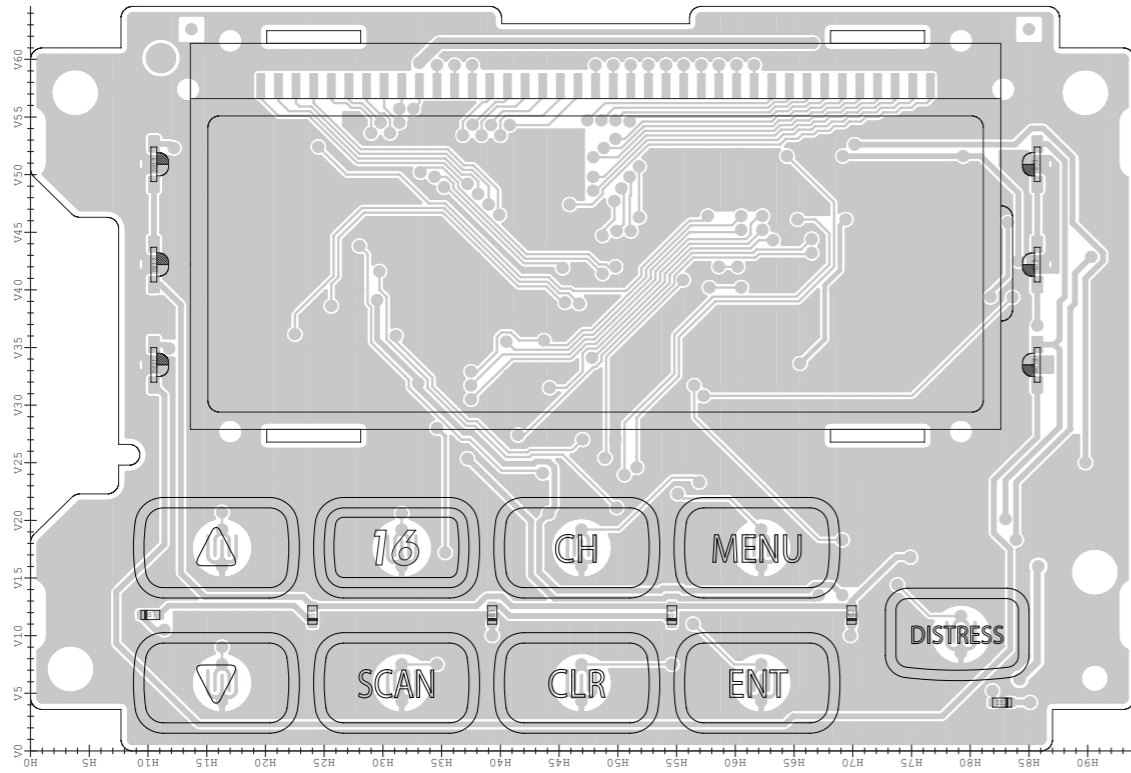
Screw abbreviations

A, B0, BT: Self-tapping PH: Pan head ZK: Black NI-ZU: Nickel-Zinc SUS: Stainless

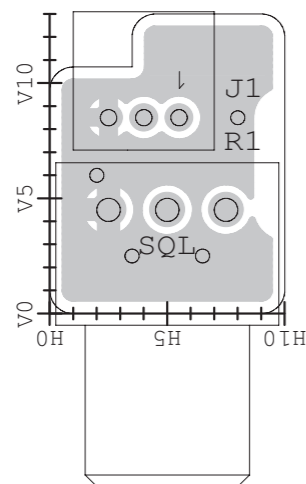


UNIT abbreviations (C): CHASSIS PARTS, (LG): LOGIC BOARD, (MA): MAIN UNIT (FR): FRONT UNIT
 (VR): VR BOARD, (SQL): SQL BOARD

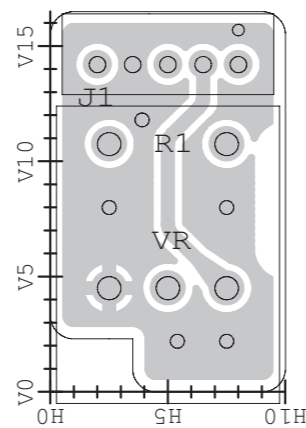
• LOGIC UNIT
(TOP VIEW)



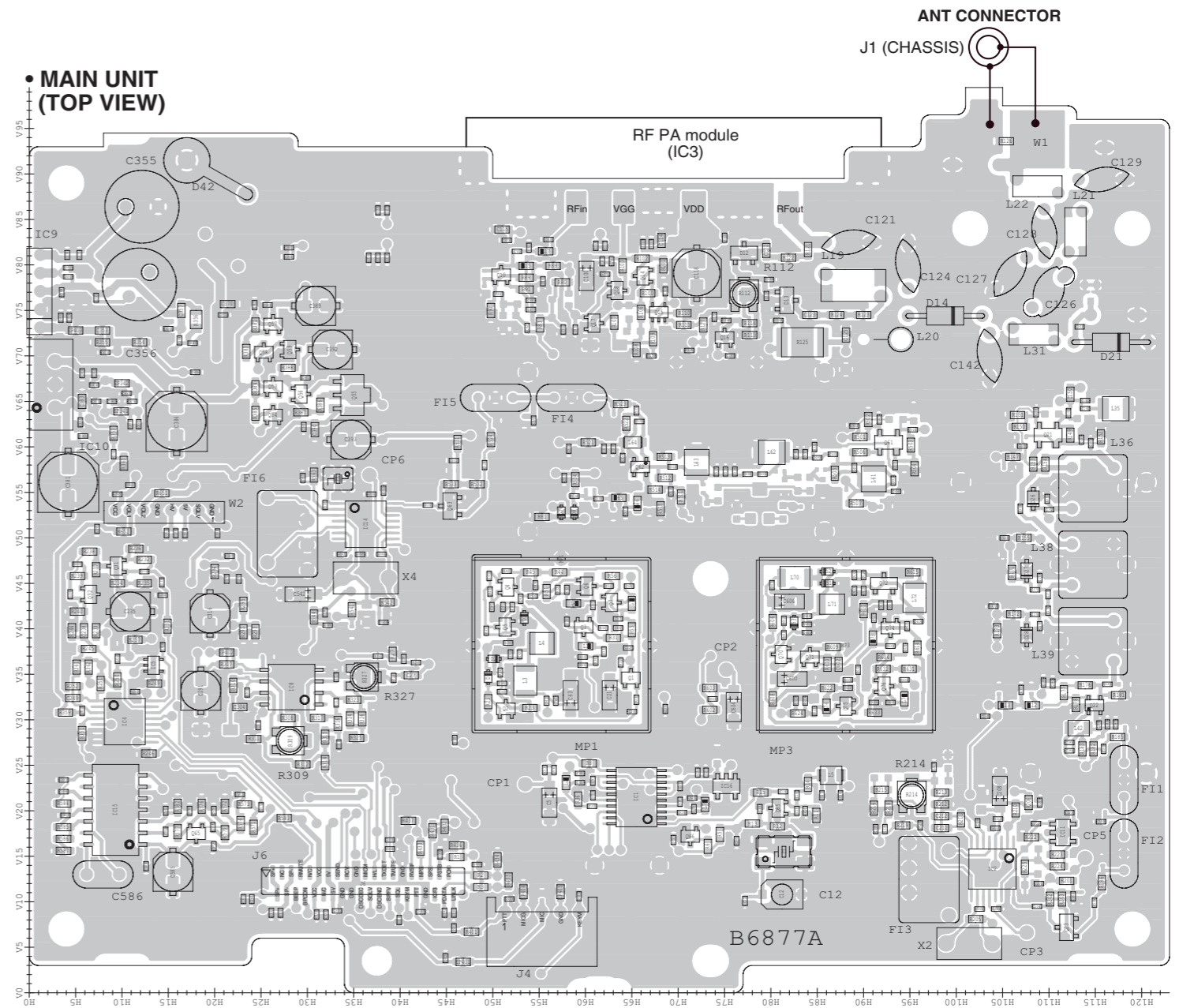
• SQL UNIT
(TOP VIEW)



• VR UNIT
(TOP VIEW)

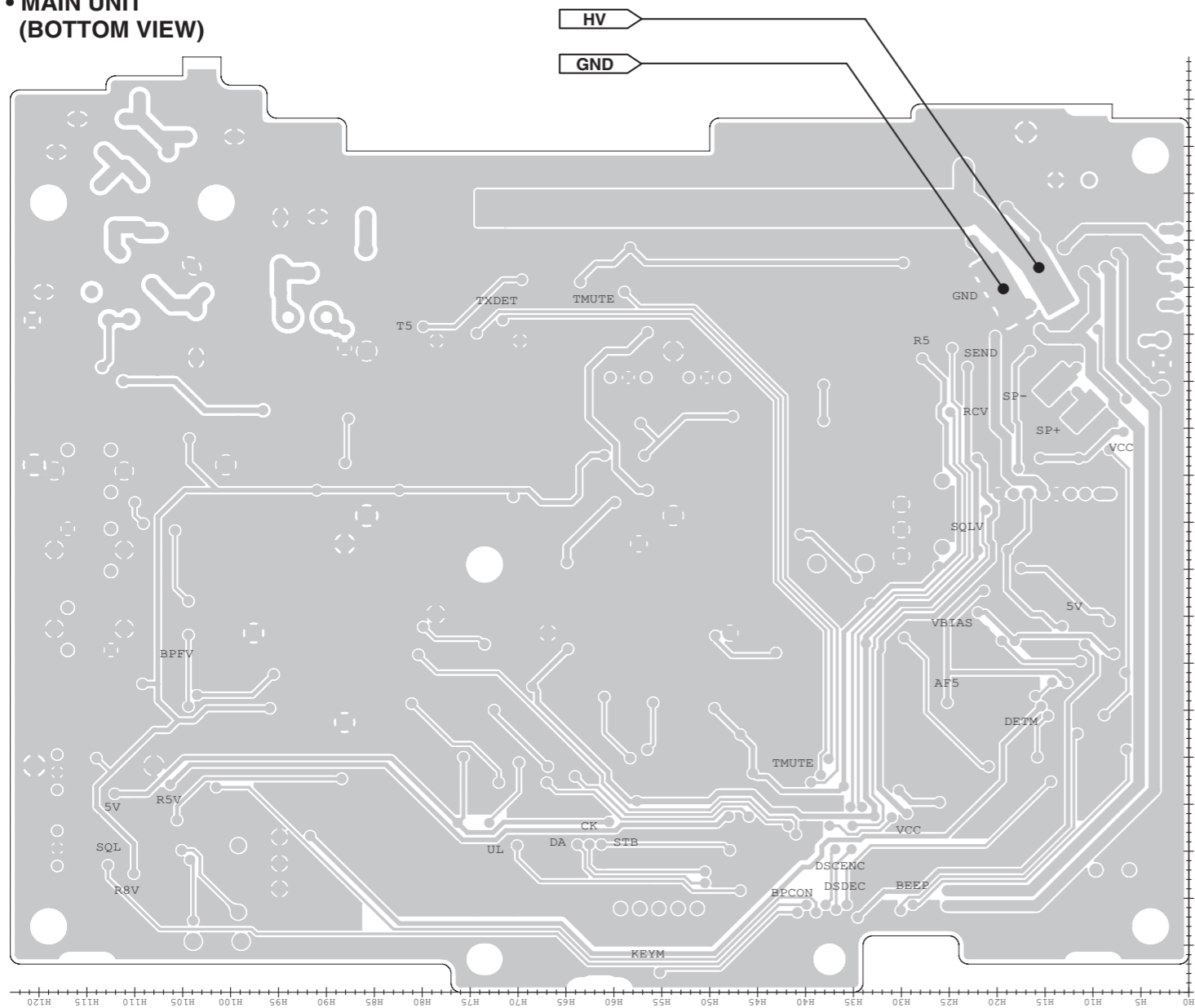


• MAIN UNIT
(TOP VIEW)

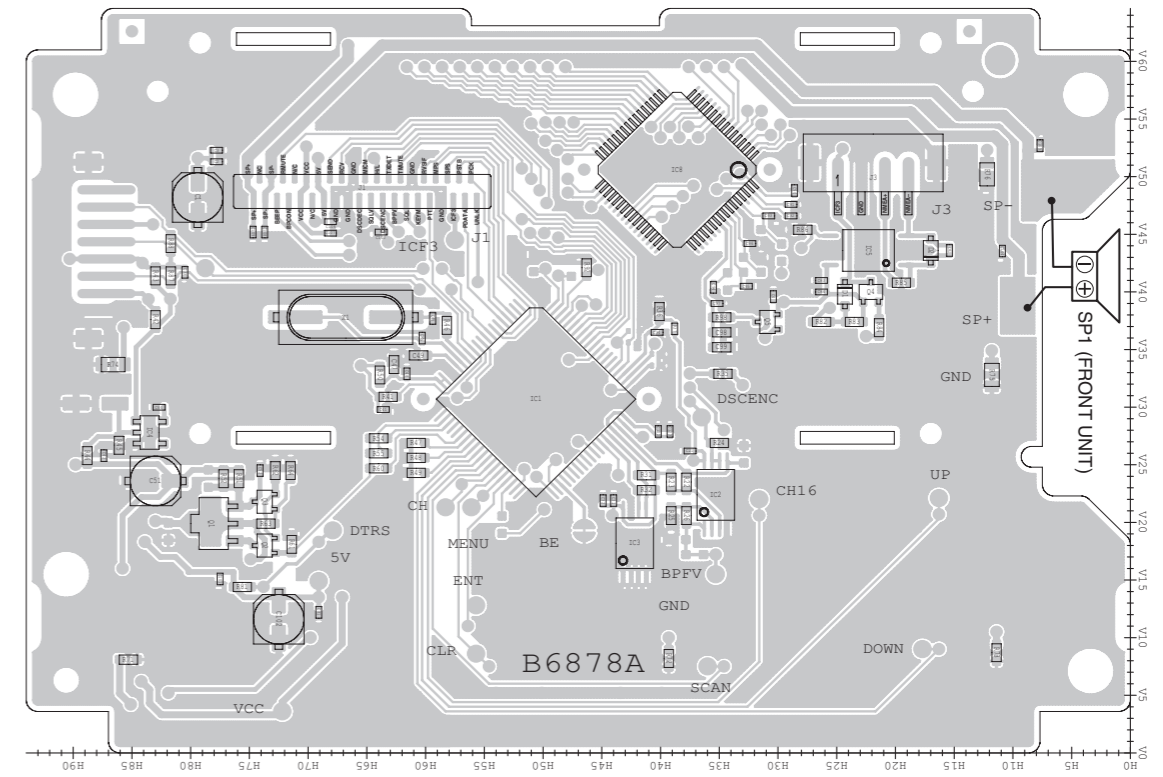


The combination of this side and the bottom side shows the board layout in the same configuration as the actual P.C.Board.

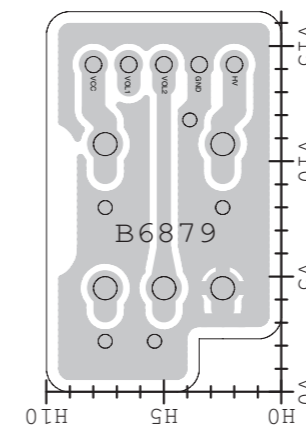
• MAIN UNIT
(BOTTOM VIEW)



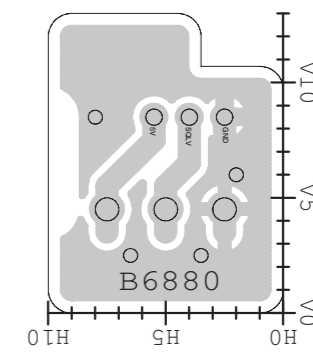
• LOGIC UNIT
(BOTTOM VIEW)



• VR UNIT
(BOTTOM VIEW)

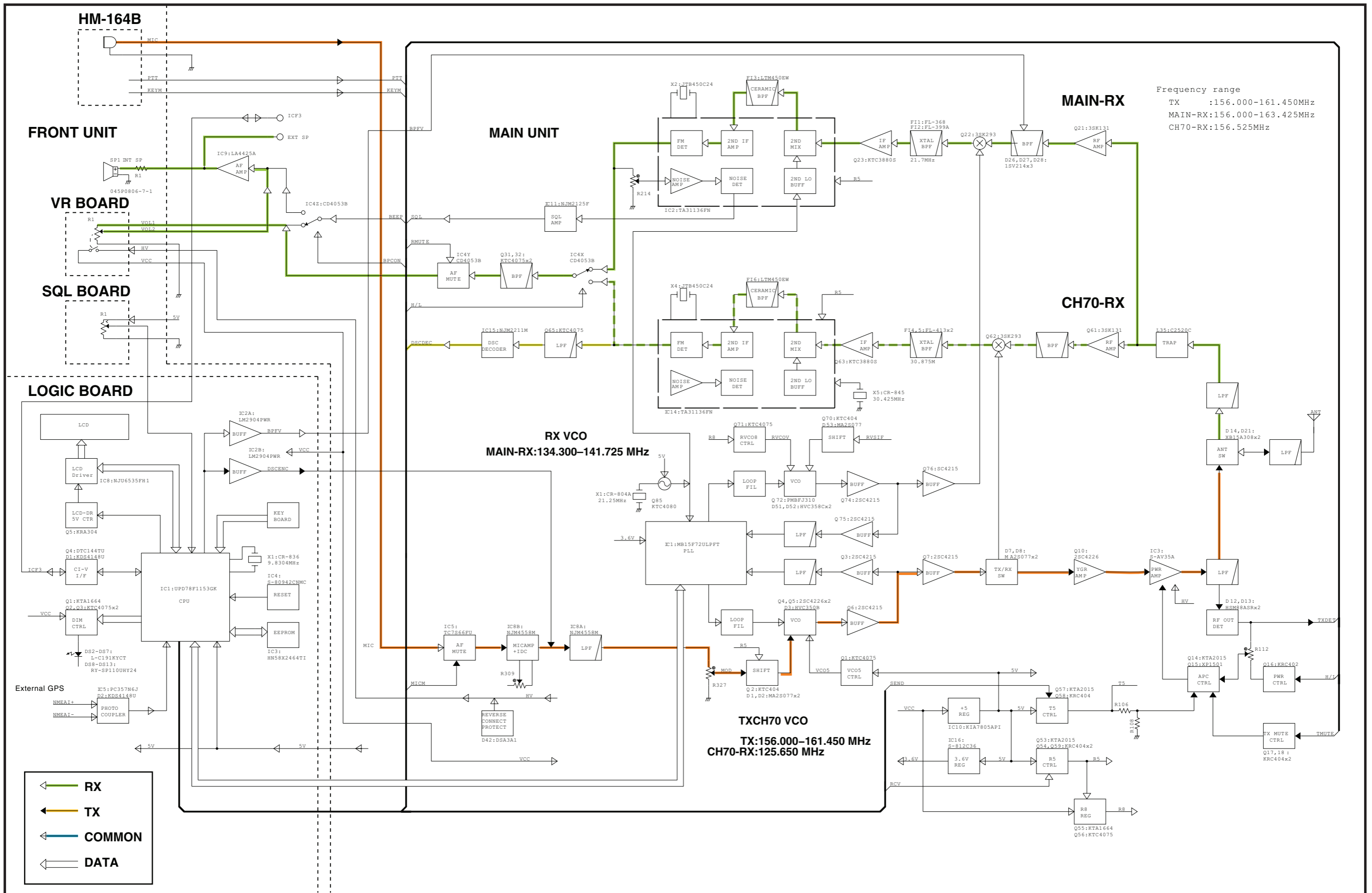


• SQL UNIT
(BOTTOM VIEW)



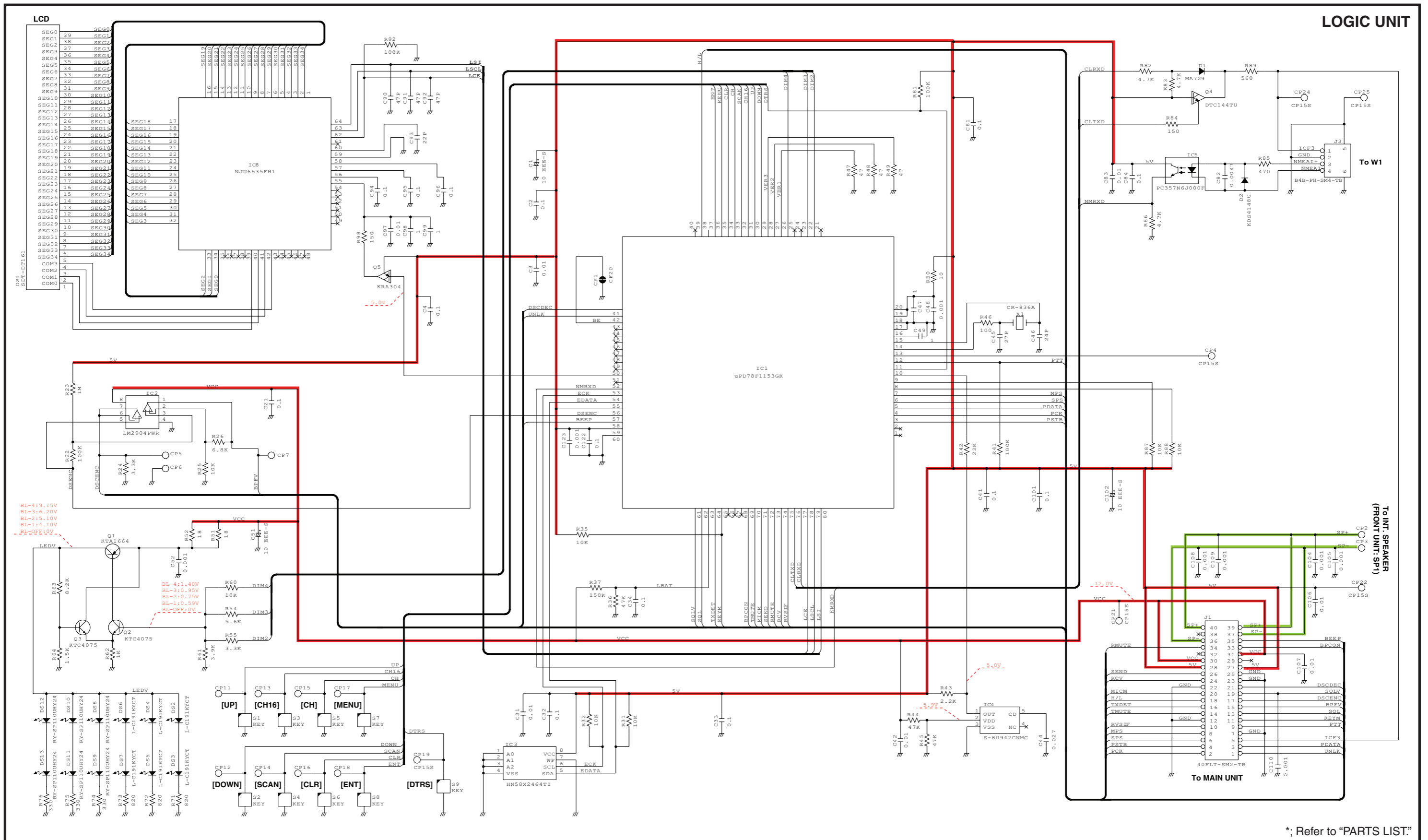
SECTION 9

BLOCK DIAGRAM



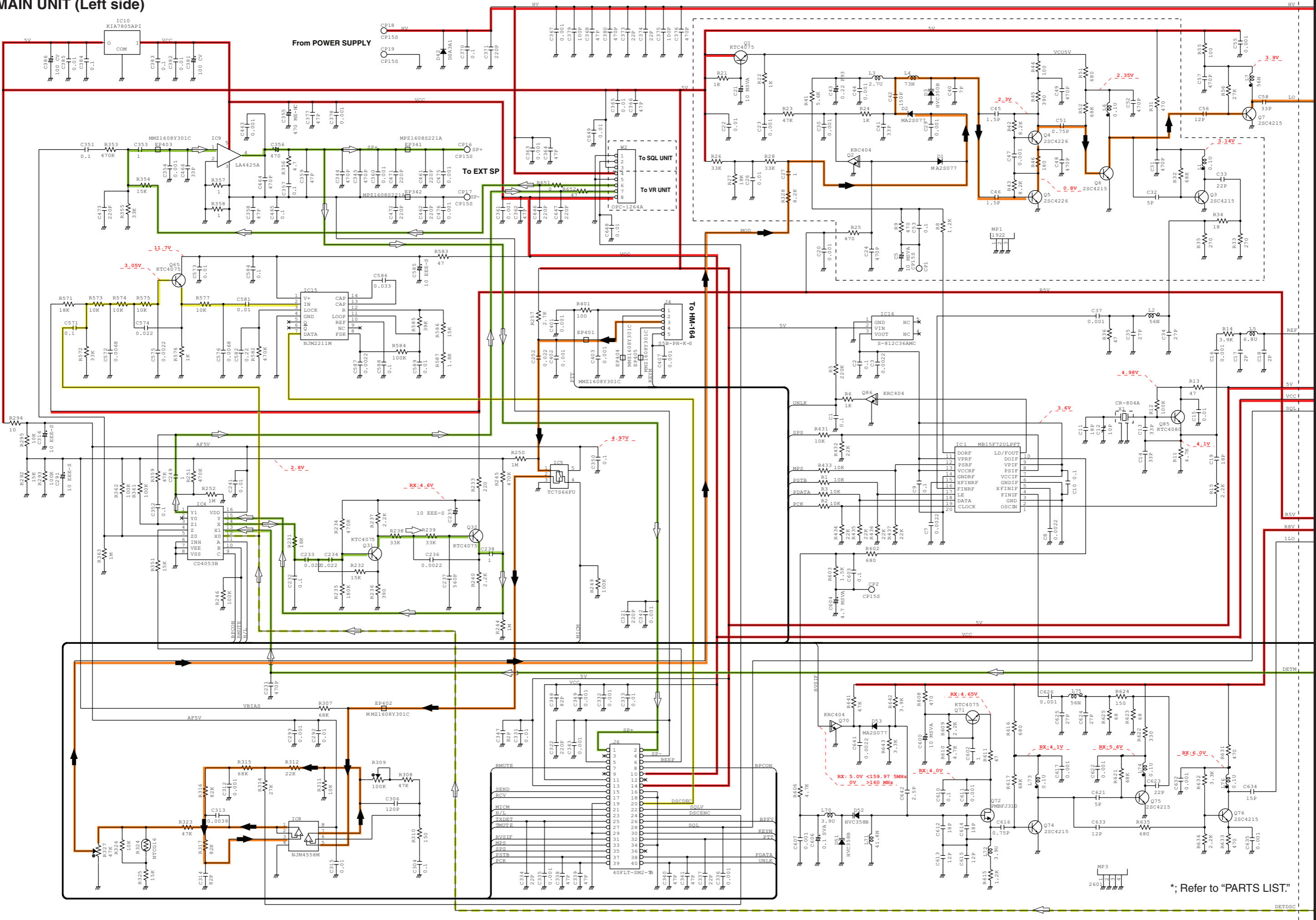
SECTION 10

VOLTAGE DIAGRAM

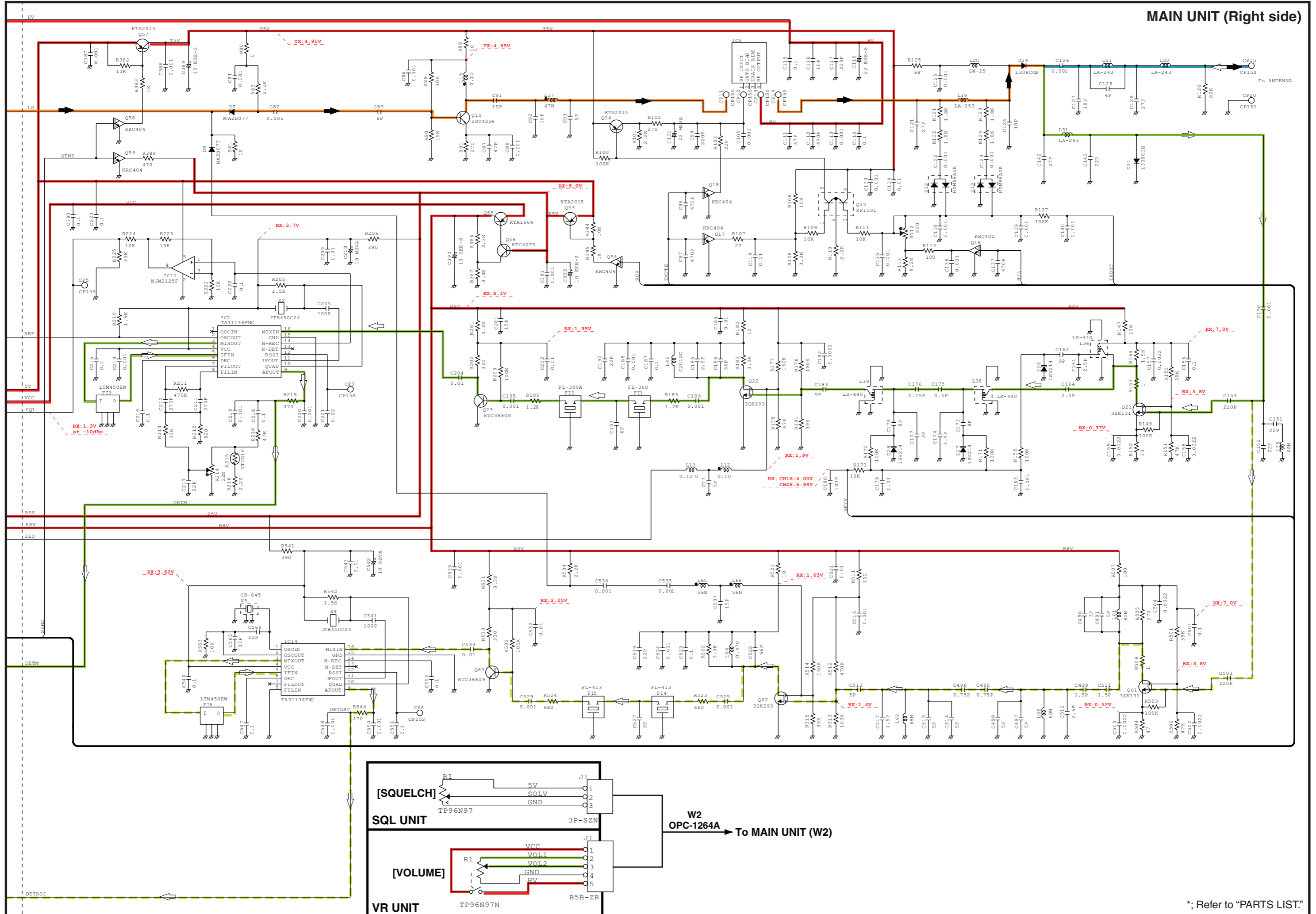


*; Refer to "PARTS LIST."

MAIN UNIT (Left side)



*; Refer to "PARTS LIST"



*; Refer to "PARTS LIST."

SECTION 11

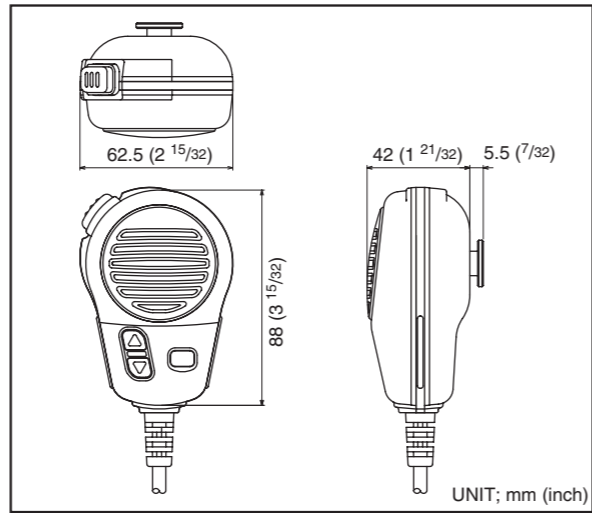
HM-164B

• CHASSIS PARTS LIST

REF NO.	ORDER NO.	DESCRIPTION	QTY.
W1	8900016570	OPC-1747	1
MP1	8210023240	2995 FRONT PANEL	1
MP2	8210020120	2715 REAR PANEL	1
MP3	8930060910	2715 KEY	1
MP4	8930060920	2715 PTT RUBBER	1
MP5	8930061970	2715 A-PTT HOLDER	1
MP6	8930060940	2715 RUBBER	1
MP7	8930060930	2715 MAIN SEAL	1
MP8	8610011600	2715 HANGER KNOB	1
MP9	8930060951	2715 BUSH PLATE-1	1
MP10	8930060990	2715 MIC SHEET	1
MP11	8930061000	2715 MIC TAPE	1
MP12	8930070800	2715 VENT SHEET	1
MP13	8930060960	O-RING (AY)	5
MP14	8930060970	O-RING (AZ)	1
MP15	8820001260	2715 SCREW 2.6X18 SUS	5
MP16	8810010230	Screw BiH M4X8 ZK	1
MP17	8850002000	SPRING WASHER M4 SUS	1
MP19	8810010240	Screw BT B0 2X6NI	2
MP20	8930061700	2715 PTT PLATE	1

Screw abbreviations A, B0, BT: Self-tapping PH: Pan head
ZK: Black NI-ZU: Nickel-Zinc SUS: Stainless

• DIMENSIONS

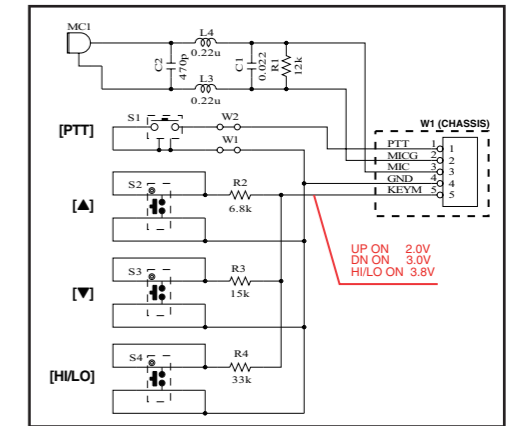


• MAIN UNIT PARTS LIST

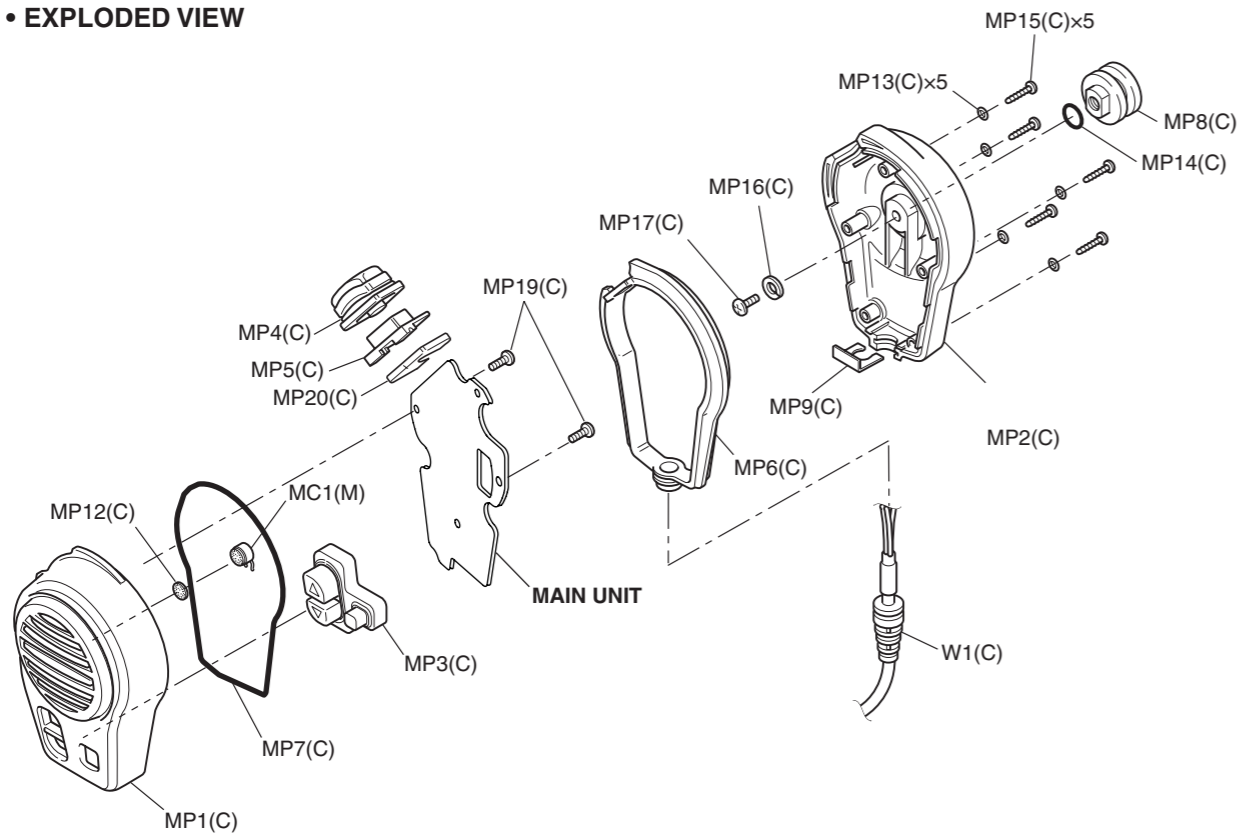
REF NO.	ORDER NO.	DESCRIPTION	M.	H/W LOCATION
L3	6180003720	COL SP0203-R22K-PF 0.22U		
L4	6180003720	COL SP0203-R22K-PF 0.22U		
R1	7010007590	RES RD1/4S 123JTA		
R2	7010007600	RES RD1/4 682JTA		
R3	7010007610	RES RD1/4 153JTA		
R4	7010007620	RES RD1/4 333JTA		
C1	4030018660	S.CER C1608 JB 1H 223K-T	B	36.85/70.8
C2	4030018670	S.CER C1608 CH 1H 471J		
MC1	7700002640	MIC KUC3523-040245		
S1	2260002780	SW SKHHLPA010		
S2	2260002790	SW SKHHAMA010		
S3	2260002790	SW SKHHAMA010		
S4	2260002790	SW SKHHAMA010		
W1	9027150010	WIR 71/98/010/X98/X98		
W2	9027150010	WIR 71/98/010/X98/X98		

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

• VOLTAGE DIAGRAM



• EXPLODED VIEW



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